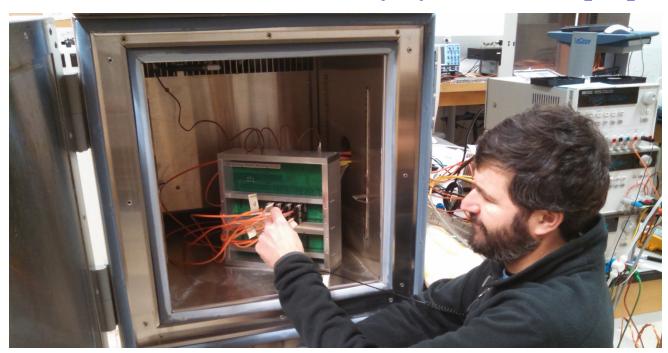


Accelerated Lifetime Test - Phase 3

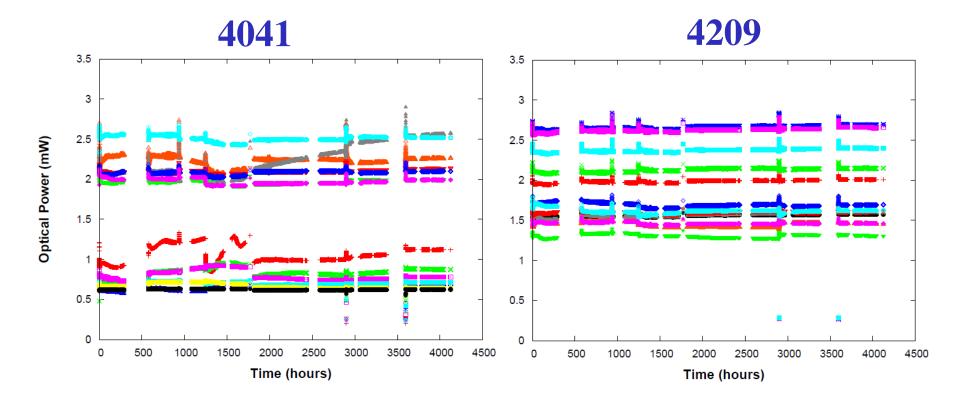
- November 2014 started four D opto-boards in 50° C / 50 % RH
 - Boards fabricated throughout production (4041, 4209, 4151, 4311)
 - Have accumulated 4125 hours running time
- Run PIN arrays at 5 V bias
- Run VCSELs at 40 MHz (50% duty cycle), 10mA pk-pk





Accelerated Lifetime Test - Phase 3

Optical power during 4125 hours in 50° C / 50 % RH





Accelerated Lifetime Test - Phase 3

Optical power during 2897 hours in 50° C / 50 % RH

