



Opto-Link R&D Status

K.K. Gan, H. Kagan, R. Kass, H. Merritt, J. Moore, A. Nagarka, S. Smith, M. Strang
The Ohio State University

June 26, 2009



Outline

- Opto-boards at 160 Mb/s with skinny wires
- VCSEL/PIN results/recommendation
- Redundancy???
- Summary

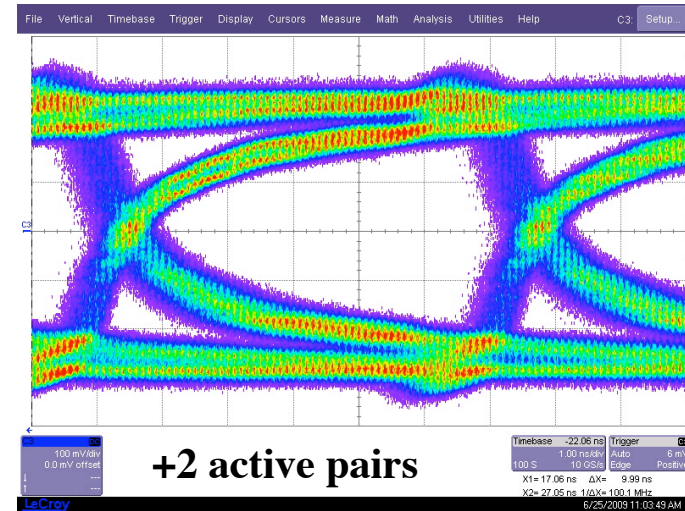
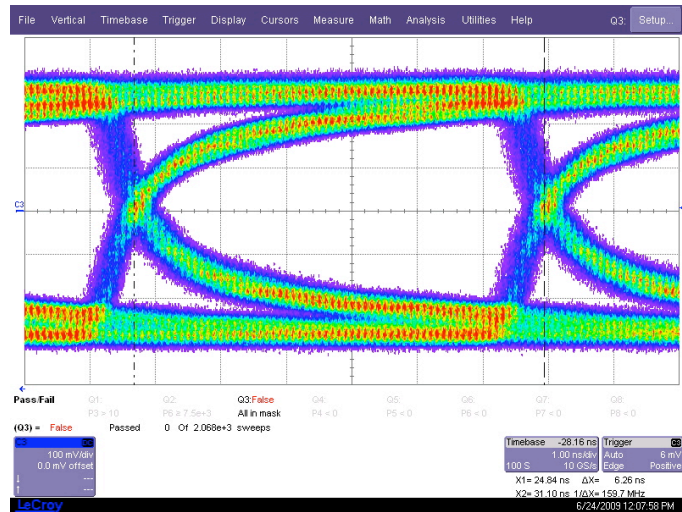


Opto-board Test

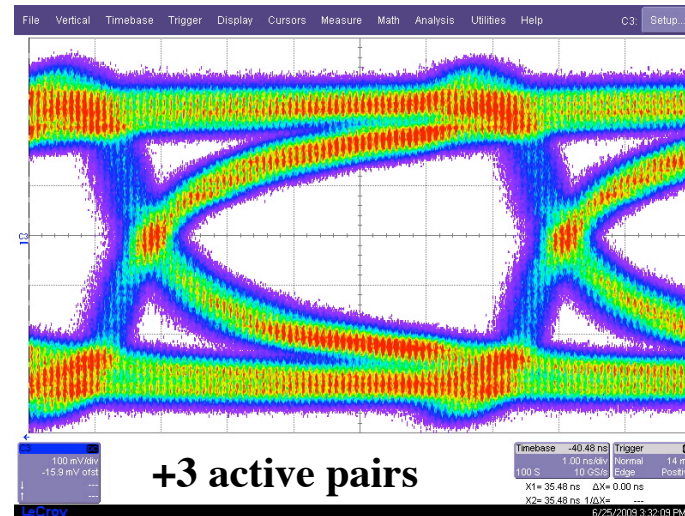
- opto-board:
 - ◆ two BeO opto-packs with AOC VCSEL array
 - ◆ one PCB opto-pack with Taiwan PIN array
 - ◆ VDC:
 - receive 160 Mb/s signal from commercial LVDS driver
- wires:
 - 30 AWG copper clad aluminum
 - 4 turns per inch (2.5 cm)
 - polyimide insulation (quad)
 - 8 pairs/bundle



Electrical Eye Diagrams

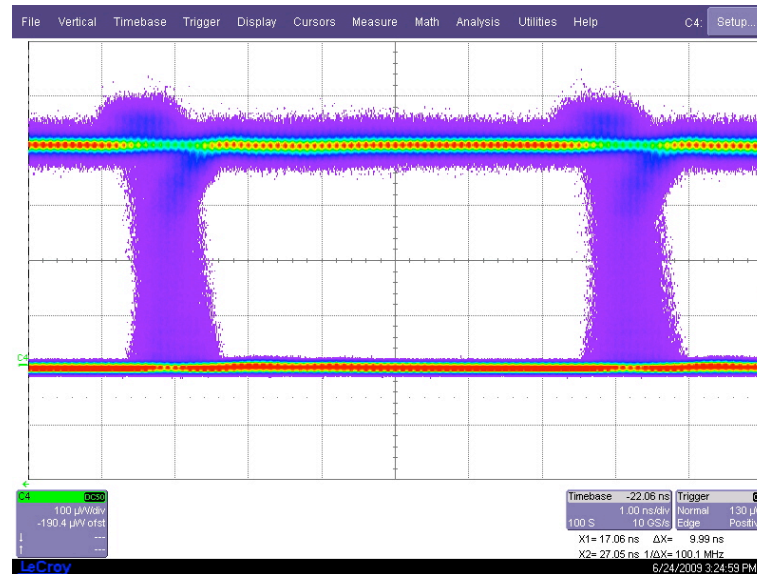


- LVDS signal after 6 m of wires looks adequate





Optical Eye Diagrams

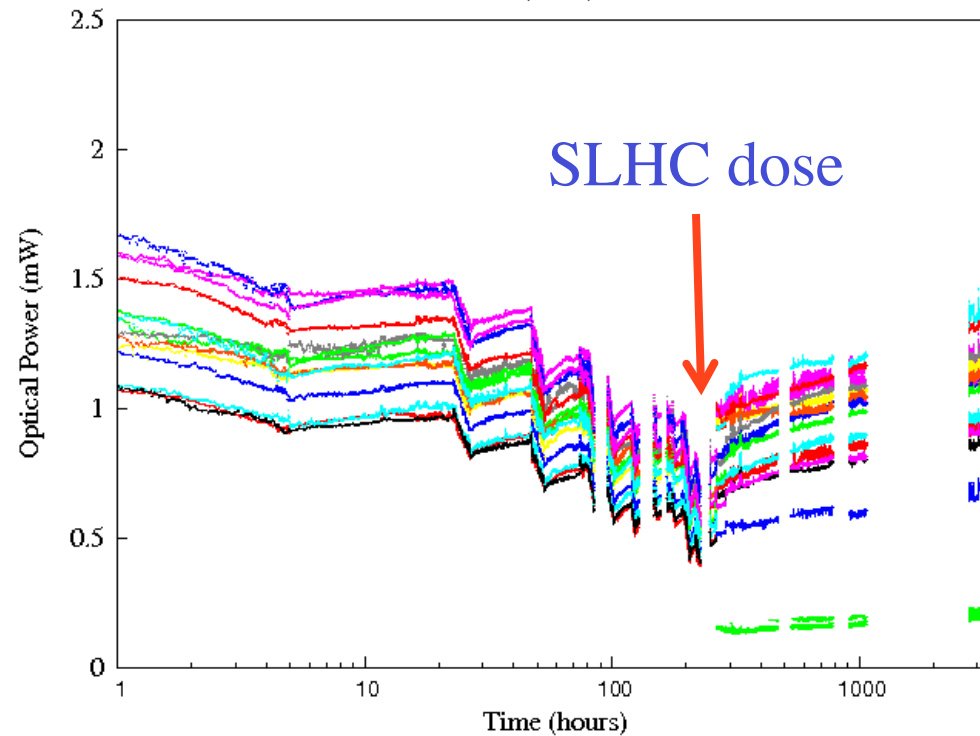


- 160 Mb/s optical signal produced by VDC looks adequate
- Plan to study eye diagram/bit error rate on opto-boards with 7 active VDC/DORIC signals received/transmitted through small wires



VCSEL Arrays

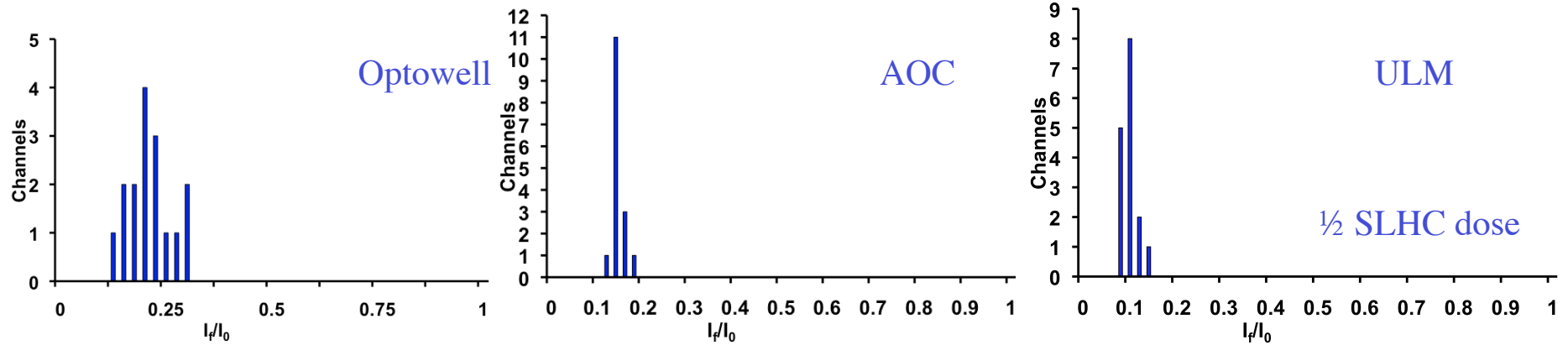
(1103)



- propose to use AOC 10 Gb/s VCSEL for on detector
- propose to irradiate 20 VCSEL opto-packs in August



PIN Arrays

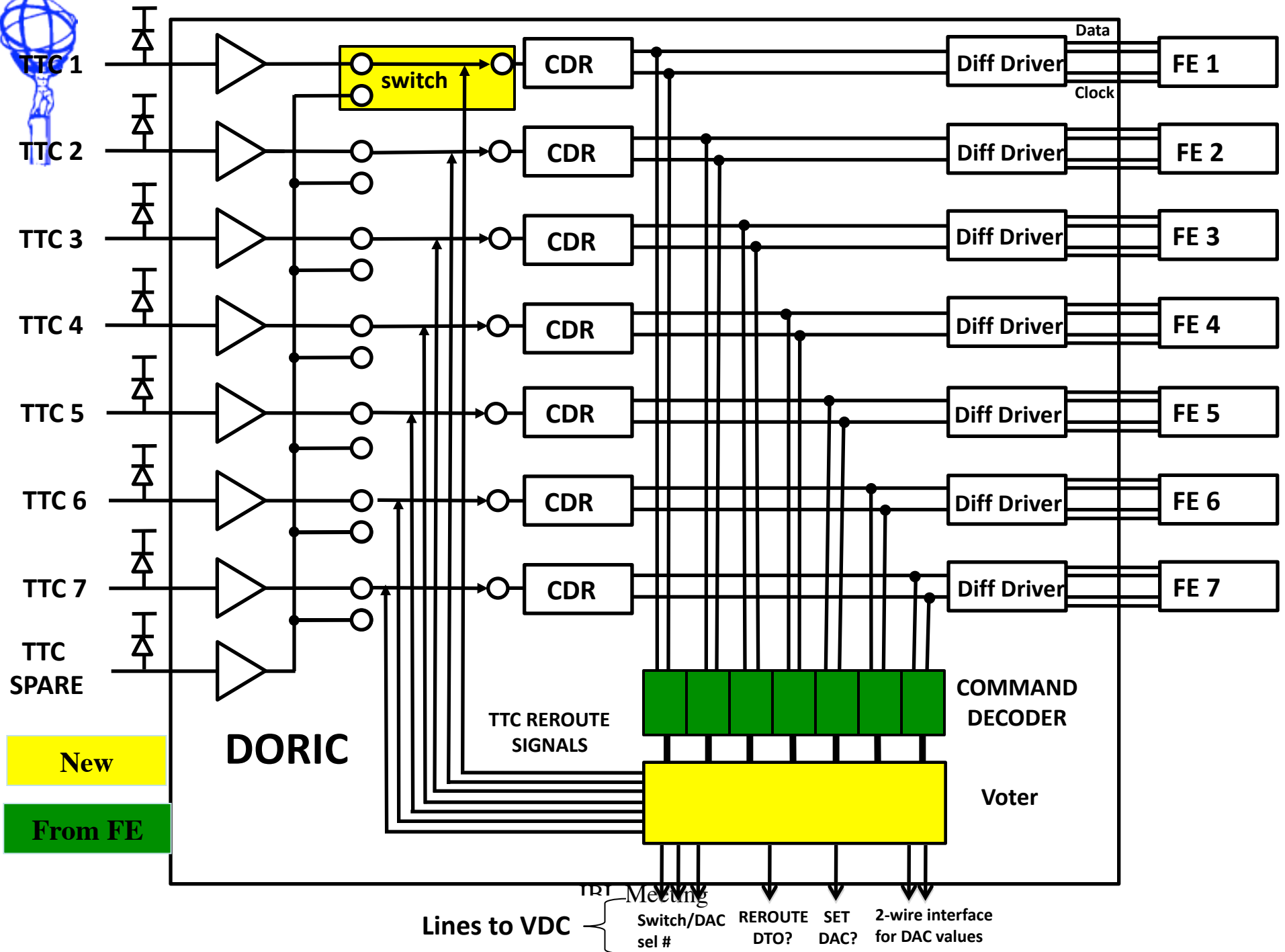


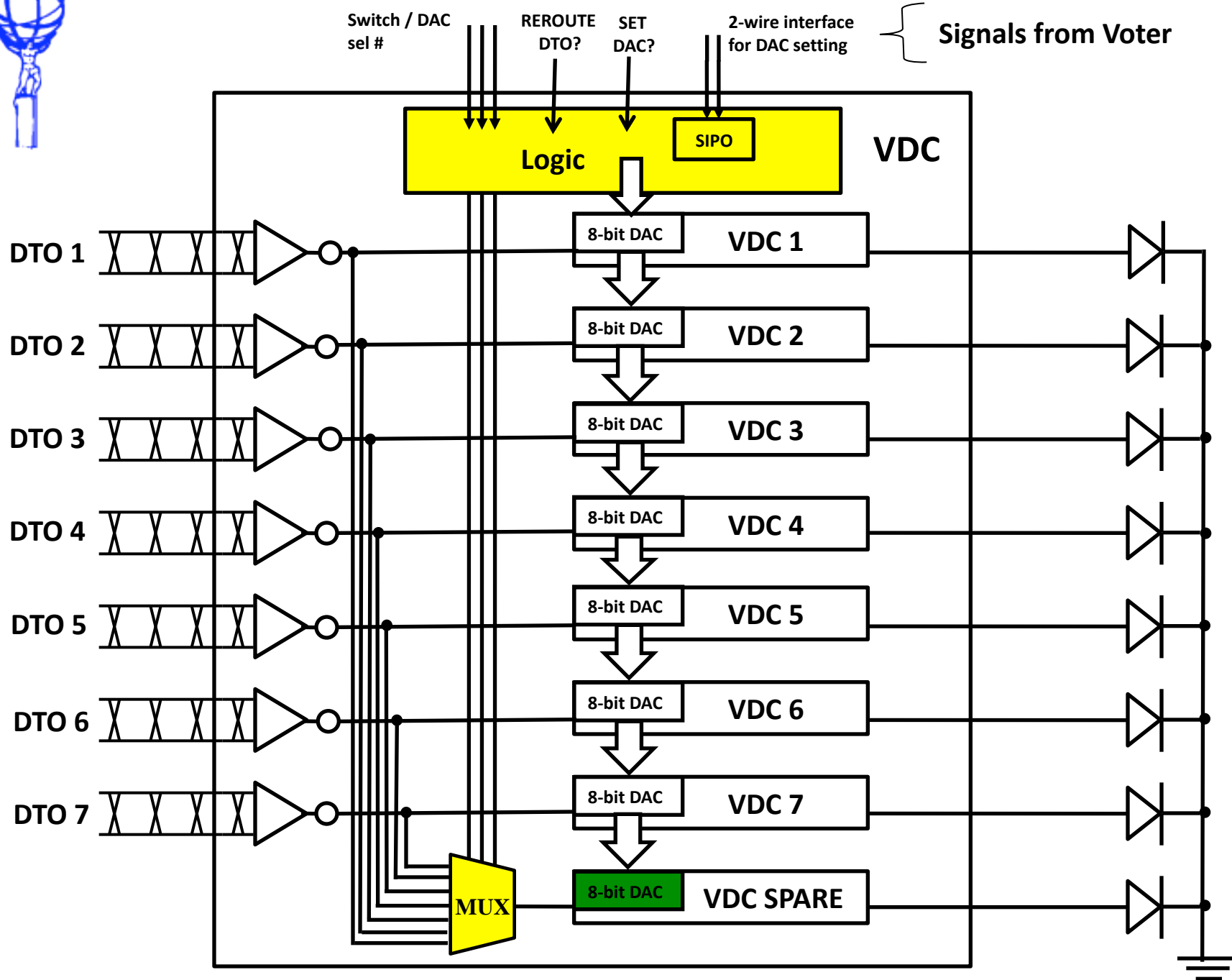
- propose to use Optowell PIN for on/off detector
- propose to irradiate 20 PIN opto-packs in August



Redundancy???

- Heated debate at last IBL meeting on the need for redundancy
 - ◆ advantage: build a more robust opto-links:
 - implement redundancy to bypass broken PIN/VCSEL
 - individual control of VCSEL currents
 - ◆ disadvantage:
 - add cost
 - require modest development effort







Summary

- VDC operates adequately after 6 m of transmission lines
- AOC VCSEL/Optowell PIN proposed for irradiation in August
- ◆ Modest effort needed to develop redundancy...