

Opto-Link Redundancy for IBL???

K.K. Gan The Ohio State University

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Outline

- Possible redundancy scheme
- If we had redundancy in present pixel detector...
- System/cost implications

IBL MB Meeting

Opto-Links of Present Pixel Detector

- opto-boards are mounted at PP0 and inaccessible
 - □ there are 21 broken links in present system (details later)
 - □ no individual control of optical power in VCSEL array
 - ➡ difficult to operate some arrays due to power spread

Possible Redundancy Scheme

- IBL opto-boards will be mounted near ID endplates:
 - ID endplates will be activated
 - ~ 2 months to open ID endplates
 - ~ 2 months to close ID endplates
 - ➡ don't expect access to opto-boards just to fix few broken links
- Current IBL design has 32 FEs on each stave
 - each opto-board serves 8 FEs
 - there is no 8-channel VCSEL/PIN array
 - □ VCSEL/PIN array contains 12 channels:
 - ➡ use 4 unused channels to bypass broken channels?
 - control of optical power of individual channels to improve the operating margin





What if we had Redundancy in Present System...

- Impact of redundancy on present broken links:
 - seven single channel failures
 - could likely be fixed with redundancy
 - one 3-channel failure and one 4-channel failure
 - could be fixed if failures are in the opto-links
 - one 7-channel failure
 - 4 channels could be fixed if failures are in the opto-links
 - ➡ most problems could be fixed if failures are in the opto-links



System/Cost Implications

• fibers:

- need 12-fiber ribbon instead of 8-fiber ribbon
 - cost increase is minimum since most cost is in polishing
- no increase in array cost since only 12-channel array is available
- need new chips: \$90k for a production run
- minimum impact on BOC since FPGA can be programmed to produce 8 output channels from 12 input channels
- add ~100kCHF to a 10MCHF detector
- implementing redundancy has no impact on IBL schedule as MOSIS offers multiple submissions/year at \$10K for 4 mm²