

A

B

C

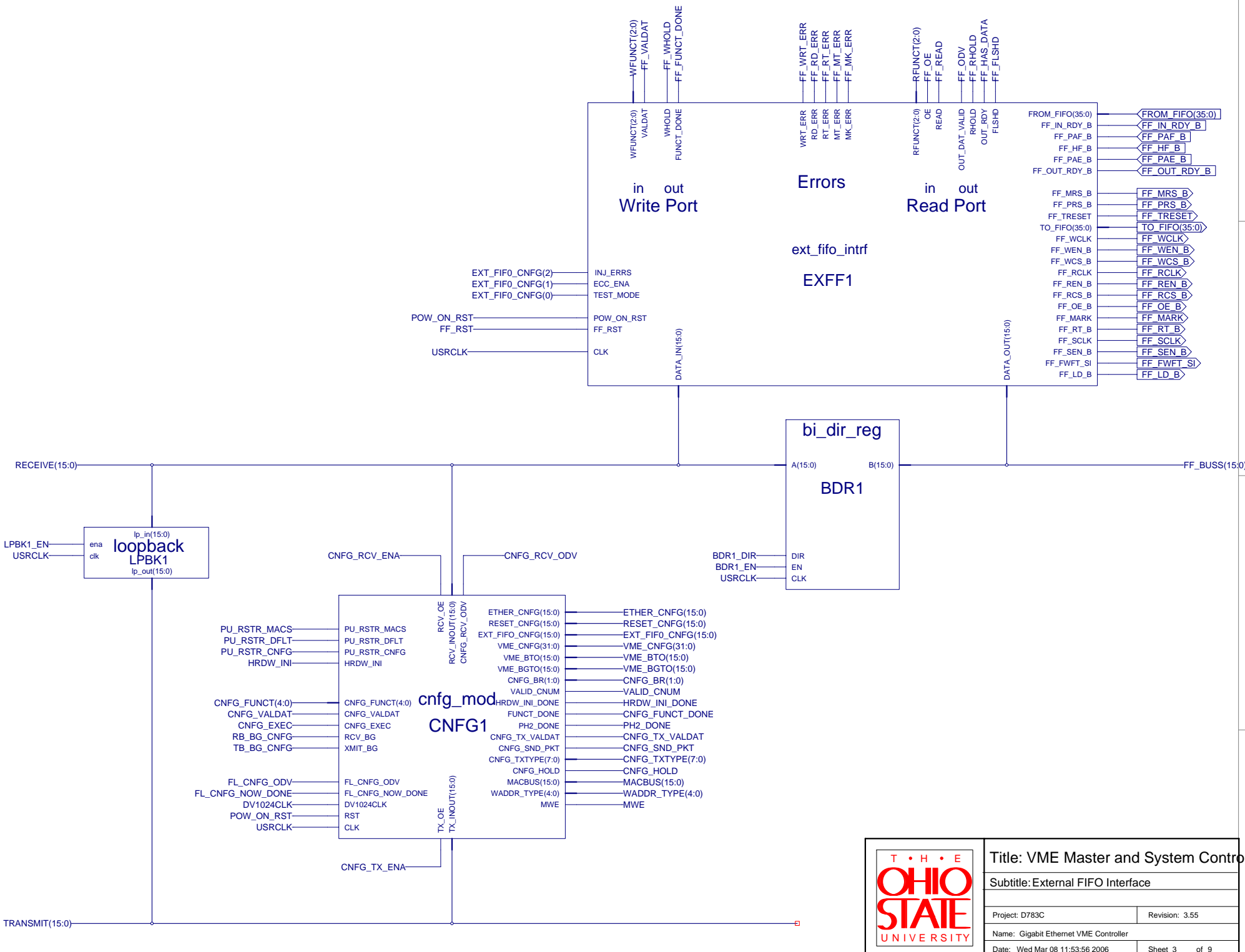
D

A

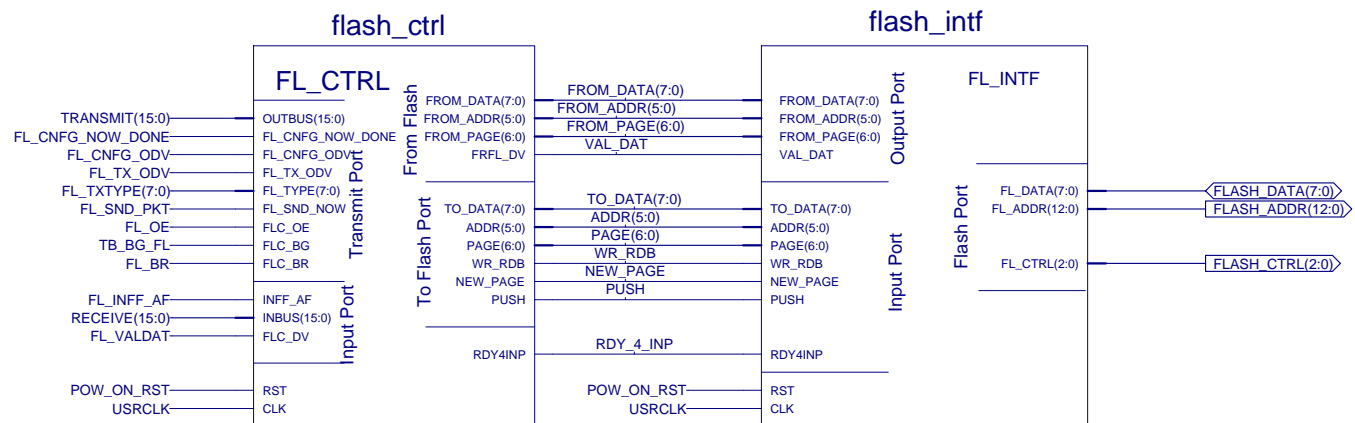
B

C

D



Title: VME Master and System Controller	
Subtitle: External FIFO Interface	
Project: D783C	Revision: 3.55
Name: Gigabit Ethernet VME Controller	
Date: Wed Mar 08 11:53:56 2006	Sheet 3 of 9



1	2	3	4	5	6	7	8
A							A
B							B
C							C
D							D
1	2	3	4	5	6	7	8



Title: VME Master and System Controller	
Subtitle: JTAG Interface	
Project: D783C	Revision: 3.55
Name: Gigabit Ethernet VME Controller	
Date: Wed Mar 08 11:53:56 2006	Sheet 5 of 9

A

B

C

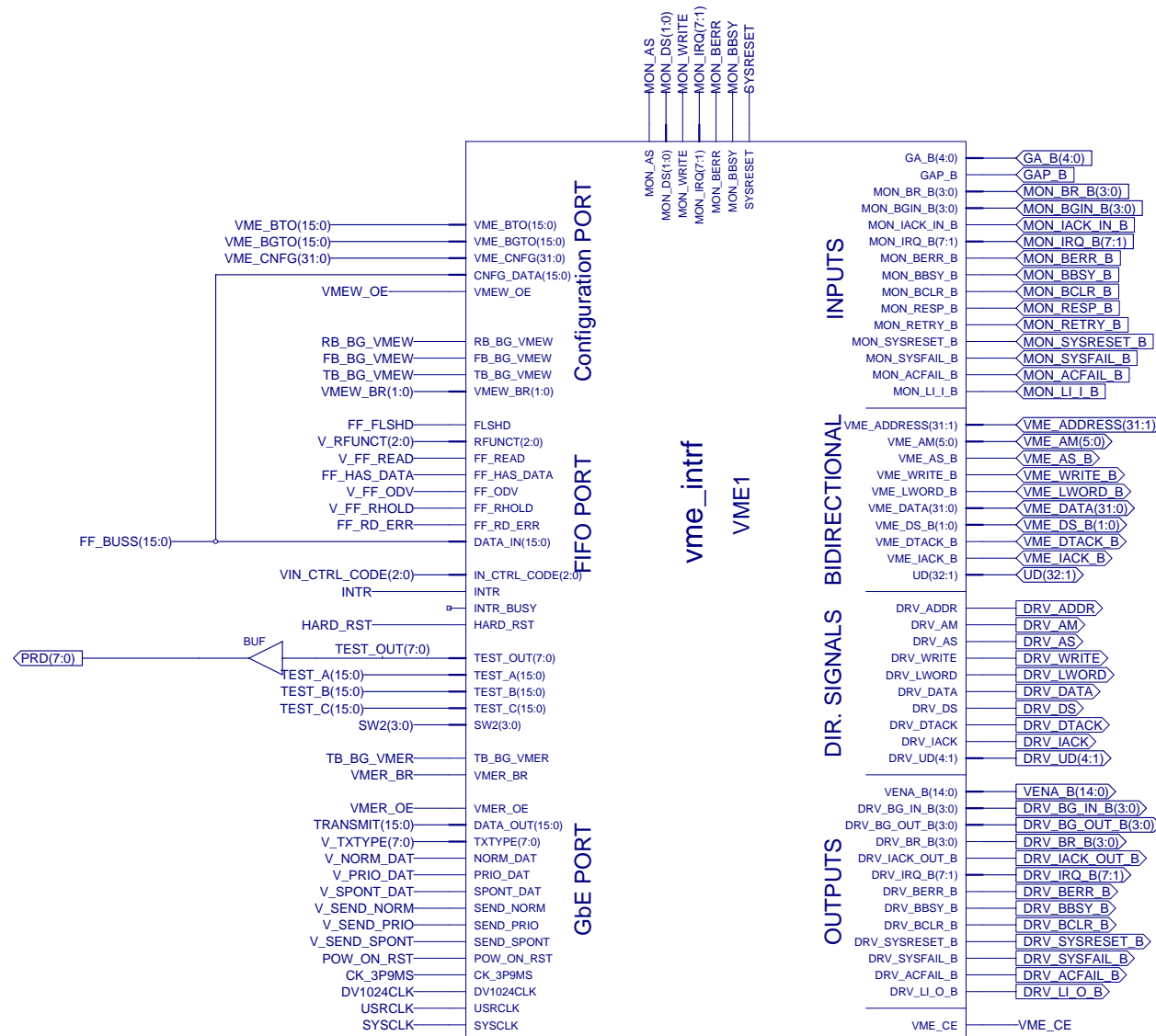
D


A

B

C

D



	<b>Title: VME Master and System Controller</b>	
	Subtitle: VME Interface	
	Project: D783C	Revision: 3.55
	Name: Gigabit Ethernet VME Controller	
	Date: Wed Mar 08 11:53:56 2006	Sheet 6 of 9

A

B

C

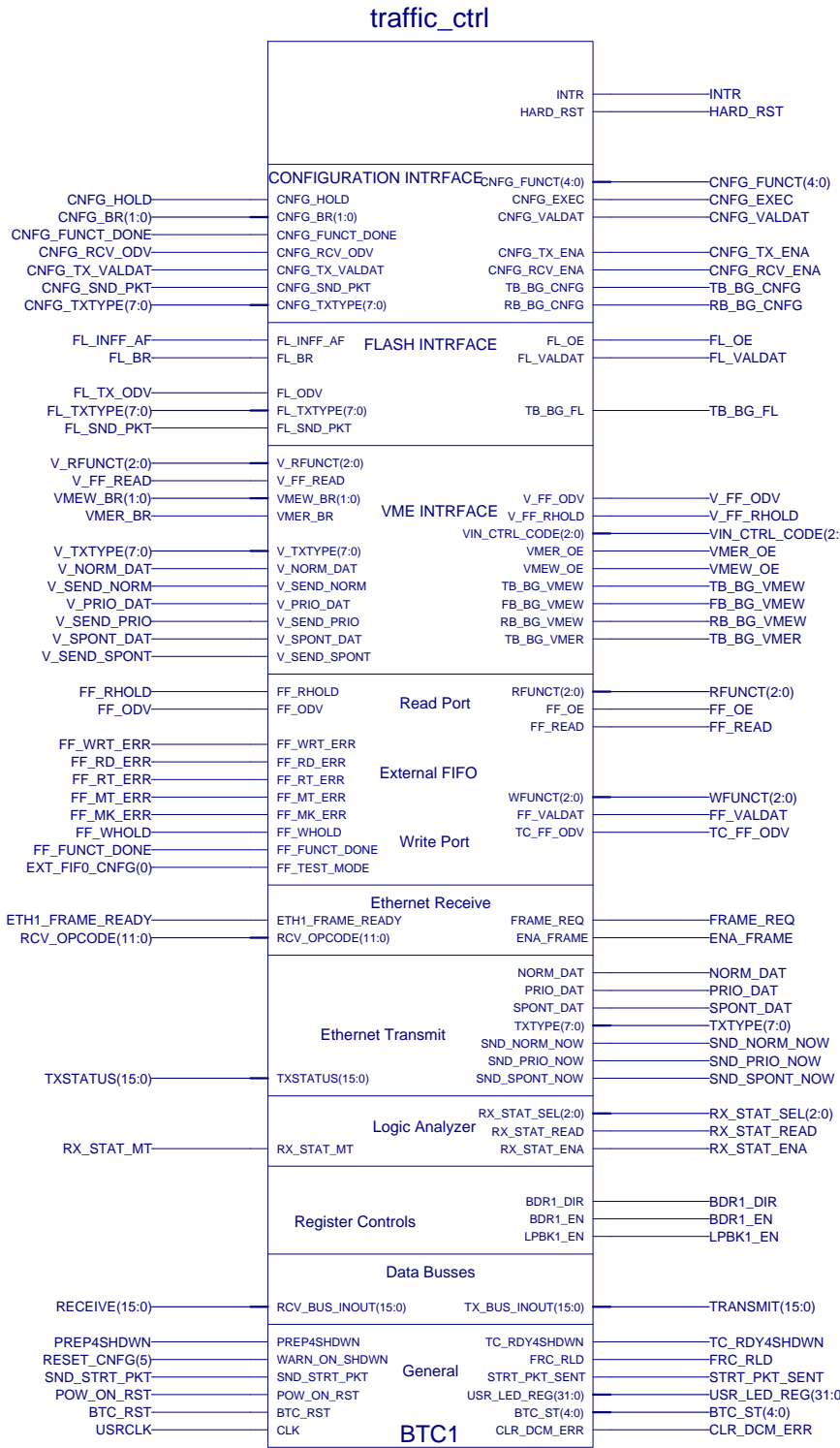
D


A

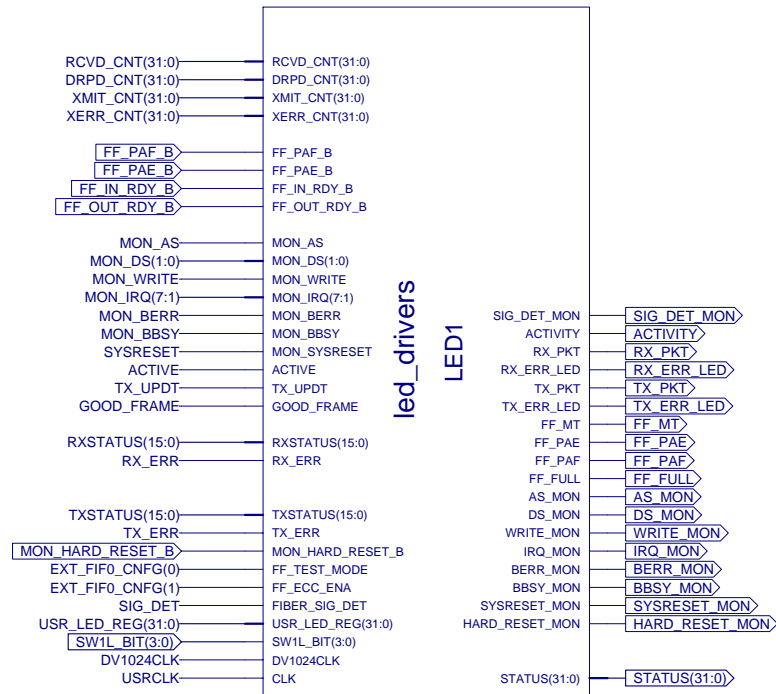
B

C

D



	<b>Title: VME Master and System Controller</b>	
	Subtitle: Bus Traffic Controller	
	Project: D783C	Revision: 3.55
	Name: Gigabit Ethernet VME Controller	
Date: Wed Mar 08 11:53:56 2006		Sheet 7 of 9



Title: VME Master and System Controller	
Subtitle: Status and Monitoring (LEDs)	
Project: D783C	Revision: 3.55
Name: Gigabit Ethernet VME Controller	
Date: Wed Mar 08 11:53:56 2006	Sheet 8 of 9



A

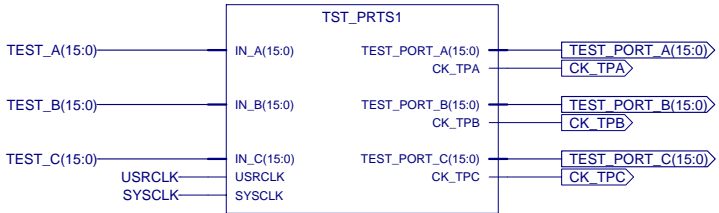
B

C

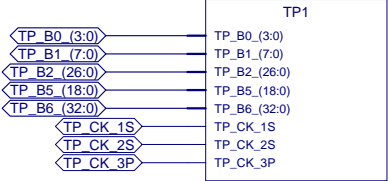
D

12345678

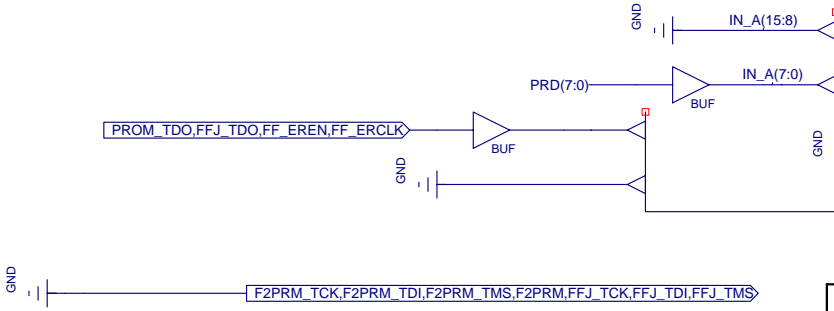
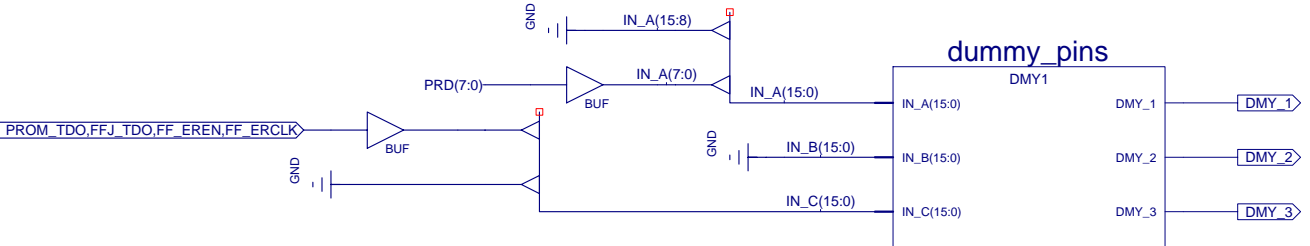
test\_ports




test\_points



dummy\_pins





Title: VME Master and System Controller

Subtitle: Test Ports and Test Points

Project: D783C	Revision: 3.55
Name: Gigabit Ethernet VME Controller	
Date: Wed Mar 08 11:53:56 2006	Sheet 9 of 9