

A

B

C

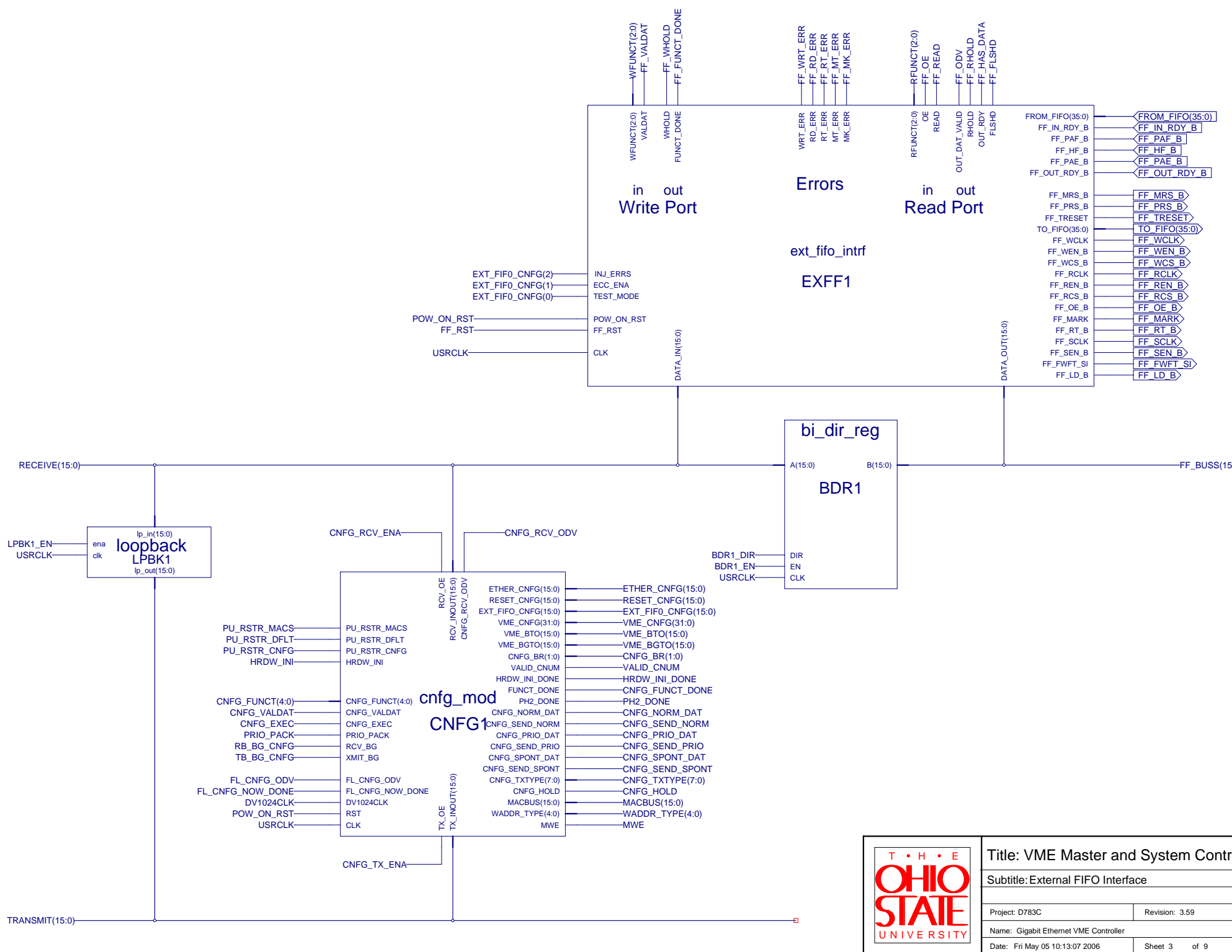
D

A

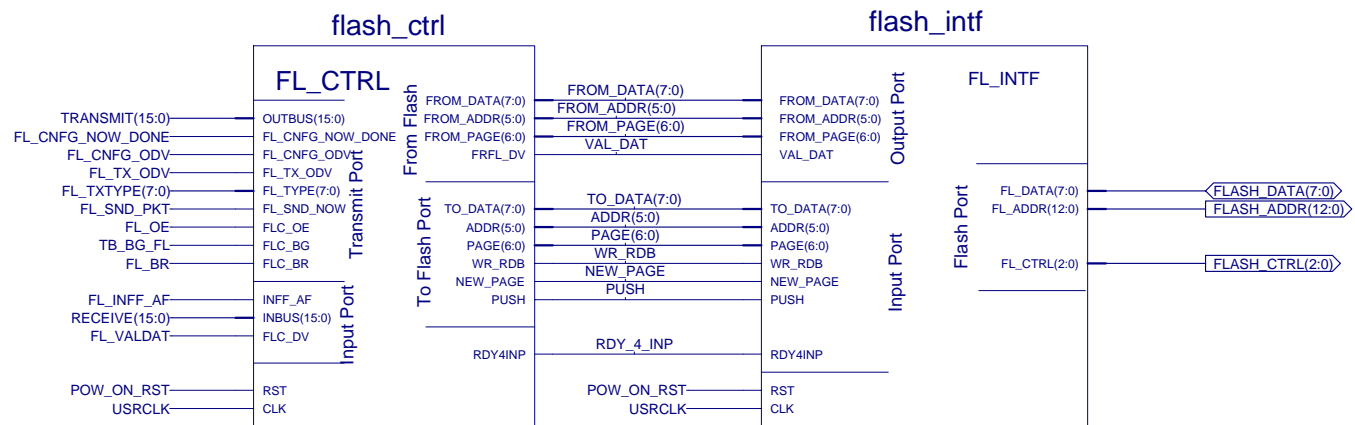
B

C

D



Title: VME Master and System Controller	
Subtitle: External FIFO Interface	
Project: D783C	Revision: 3.59
Name: Gigabit Ethernet VME Controller	
Date: Fri May 05 10:13:07 2006	Sheet 3 of 9



	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
	1	2	3	4	5	6	7	8	



Title: VME Master and System Controller	
Subtitle: JTAG Interface	
Project: D783C	Revision: 3.59
Name: Gigabit Ethernet VME Controller	
Date: Fri May 05 10:13:07 2006	Sheet 5 of 9

A

B

C

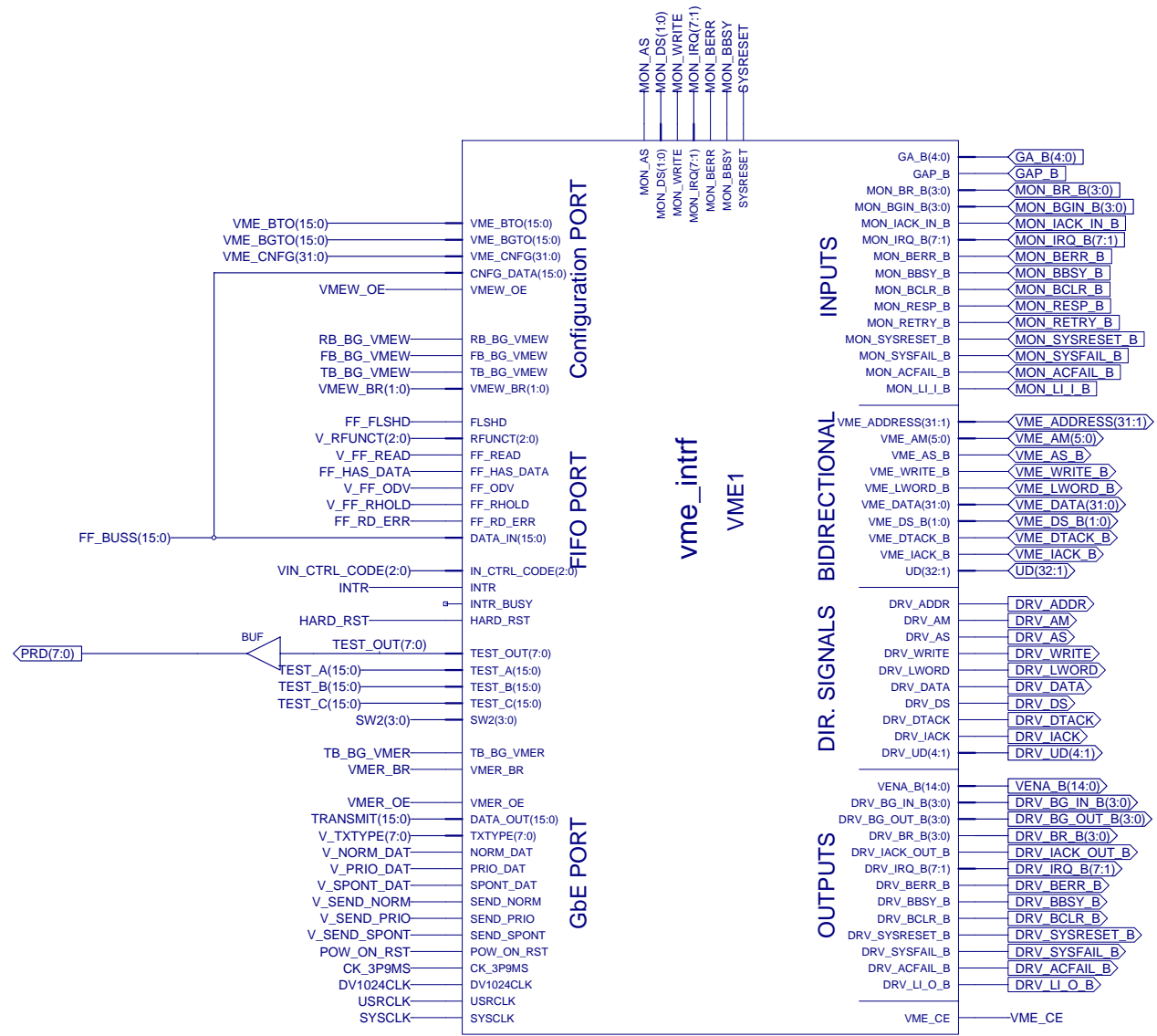
D


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Title: VME Master and System Controller

Subtitle: VME Interface

Project: D783C	Revision: 3.59
Name: Gigabit Ethernet VME Controller	
Date: Fri May 05 10:13:07 2006	Sheet 6 of 9

A

B

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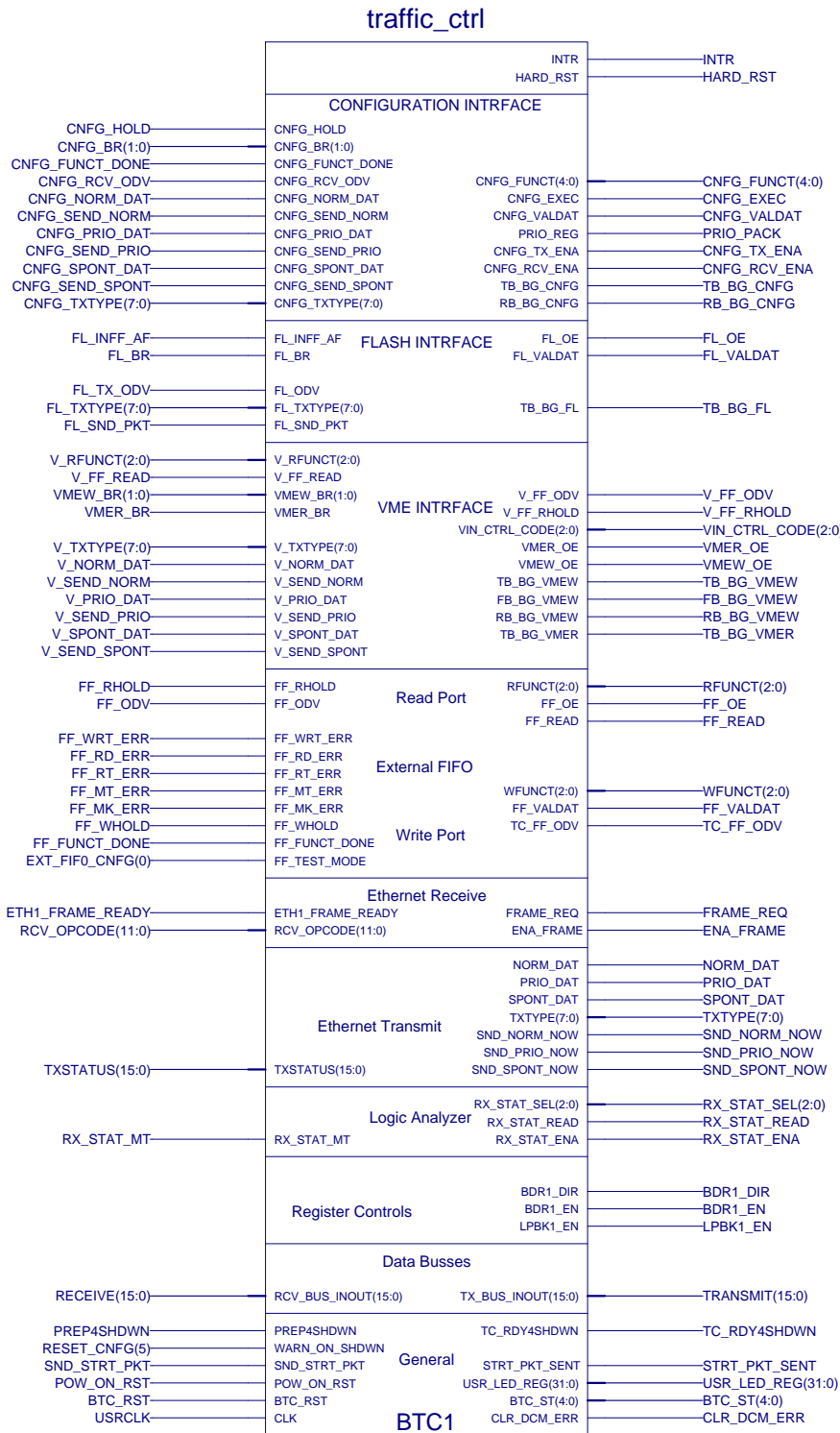
D

A

B

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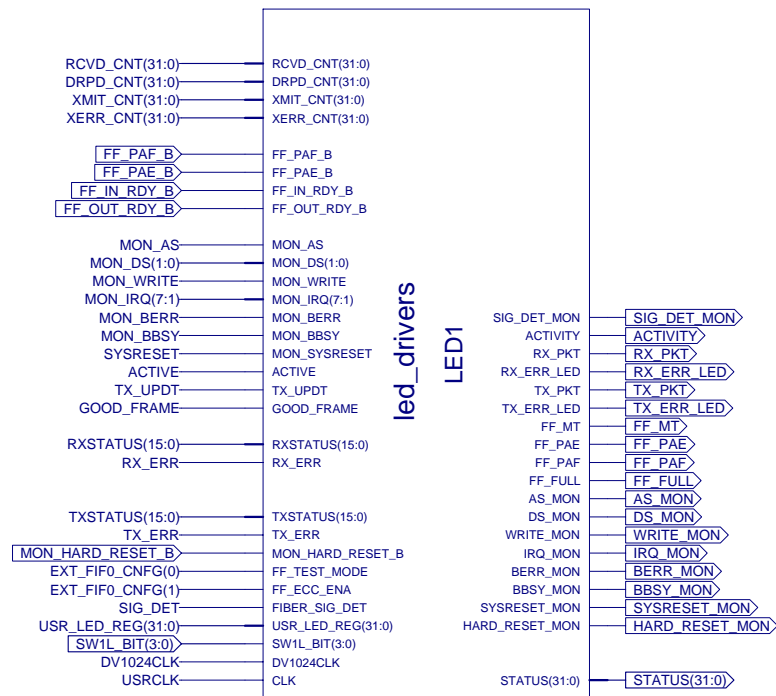
Title: VME Master and System Controller

Subtitle: Bus Traffic Controller

Project: D783C Revision: 3.59

Name: Gigabit Ethernet VME Controller

Date: Fri May 05 10:13:07 2006 Sheet 7 of 9



Title: VME Master and System Controller	
Subtitle: Status and Monitoring (LEDs)	
Project: D783C	Revision: 3.59
Name: Gigabit Ethernet VME Controller	
Date: Fri May 05 10:13:07 2006	Sheet 8 of 9

A

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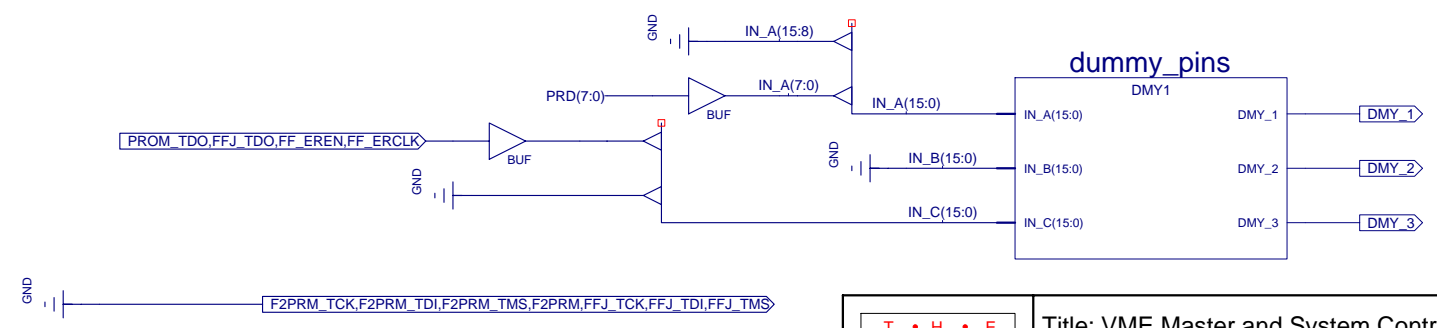
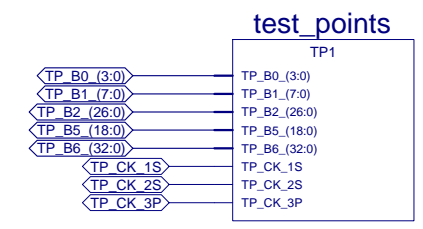
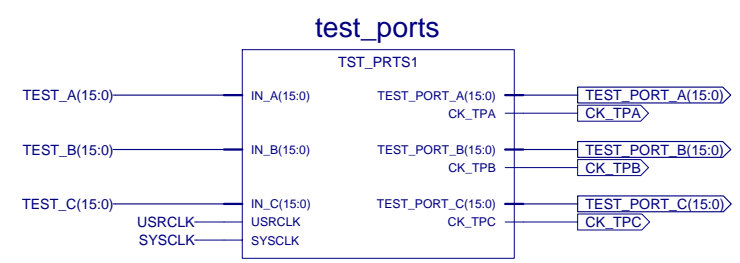
12345678


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D





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Title: VME Master and System Controller

Subtitle: Test Ports and Test Points

Project: D783C	Revision: 3.59
Name: Gigabit Ethernet VME Controller	
Date: Fri May 05 10:13:07 2006	Sheet 9 of 9