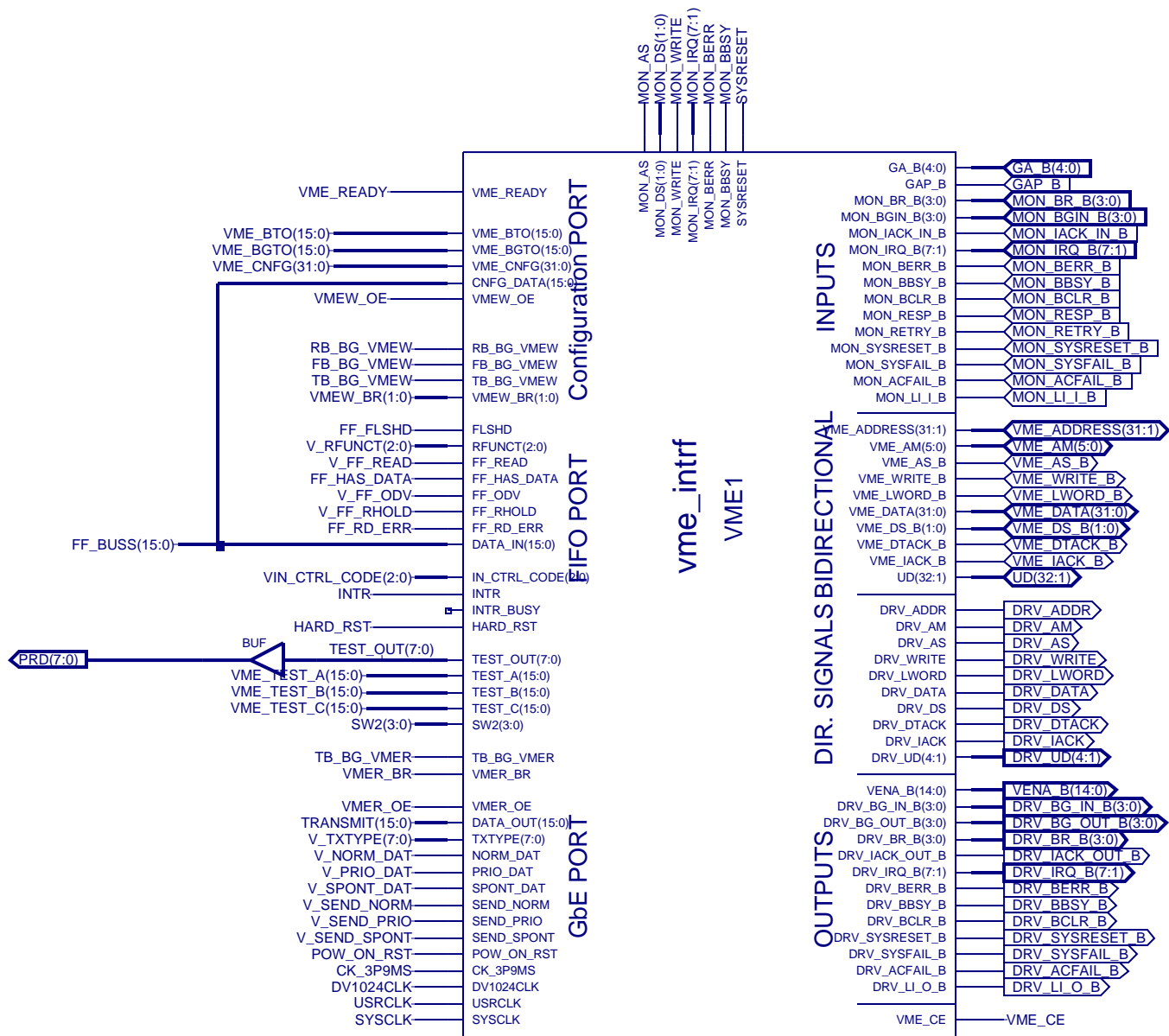


Title:VME Master and System Con	
SubtitleFlash Memory Interface	
Project:D783C	Revision: 3.6F
Name: Gigabit Ethernet VME Controller	
Date: Wed Nov 15 13:40:31 2006	Sheet 4 of 9

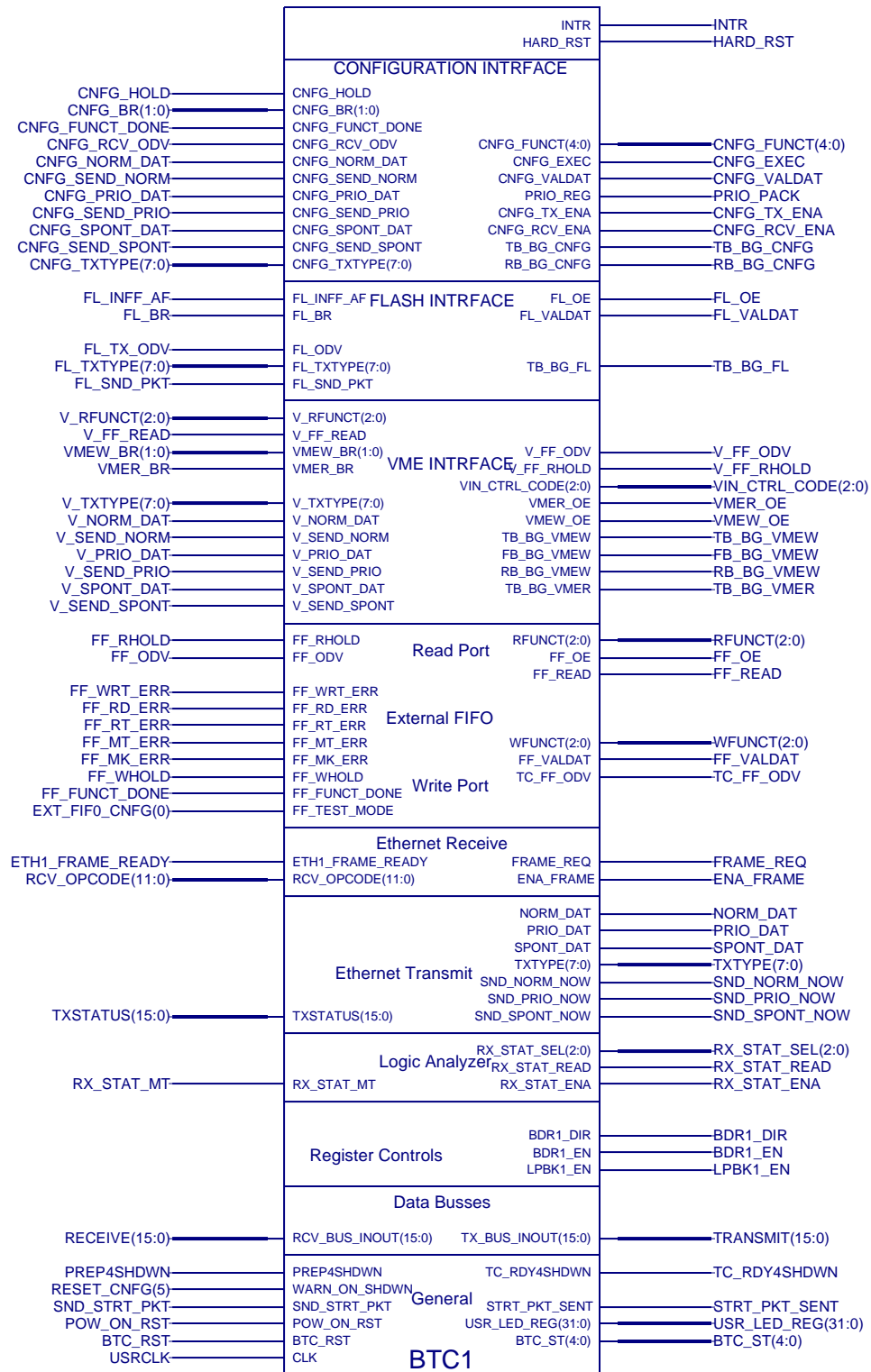


Title:VME Master and System Con	
SubtitleJTAG Interface	
Project:D783C	Revision: 3.6F
Name: Gigabit Ethernet VME Controller	
Date: Wed Nov 15 13:40:31 2006	Sheet 5 of 9



Title:VME Master and System Con	
Subtitle/VME Interface	
Project:D783C	Revision: 3.6F
Name: Gigabit Ethernet VME Controller	
Date: Wed Nov 15 13:40:31 2006	Sheet 6 of 9

traffic_ctrl



Title:VME Master and System Con

SubtitleBus Traffic Controller

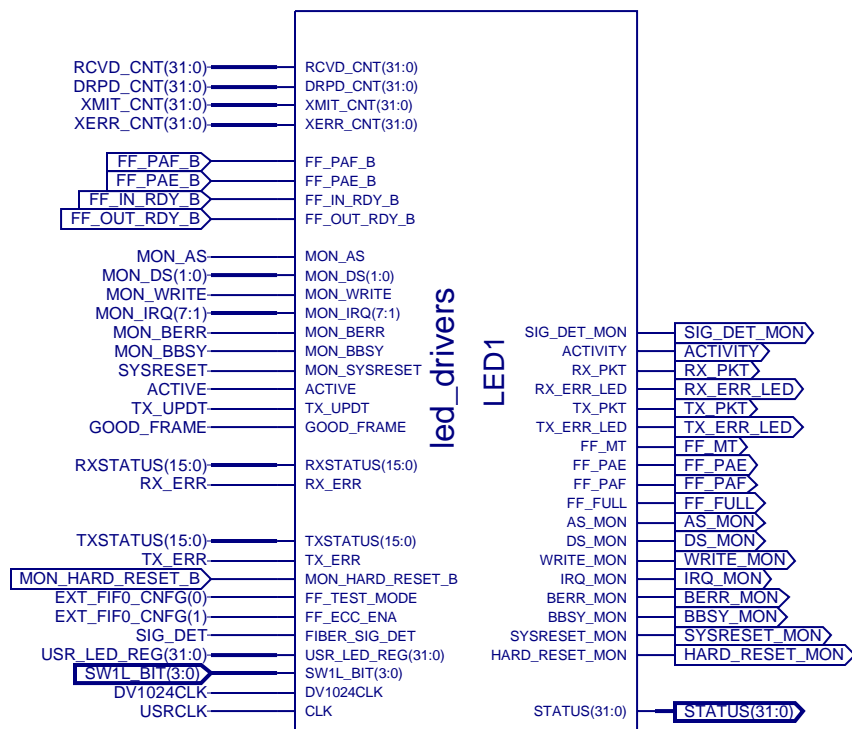
Project:D783C

Revision: 3.6F

Name: Gigabit Ethernet VME Controller

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Title:VME Master and System Con

Subtitle:Status and Monitoring (LEDs)

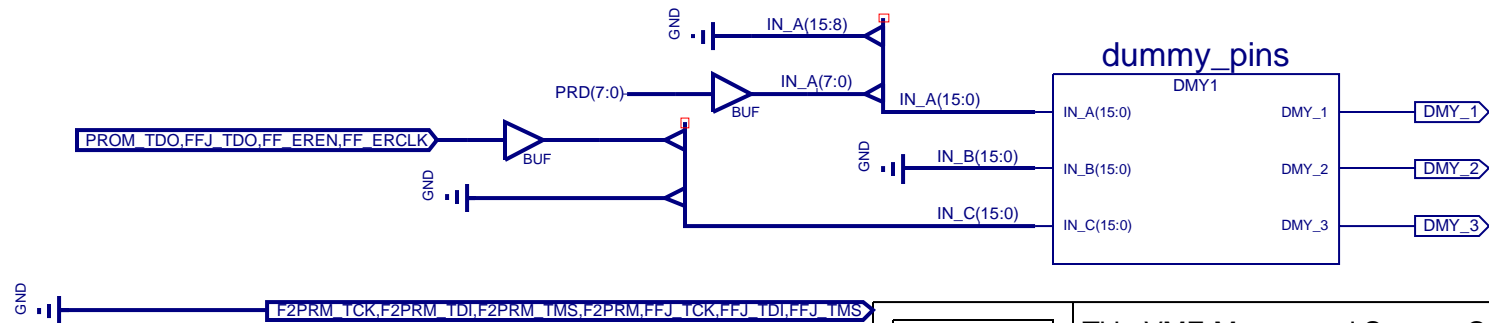
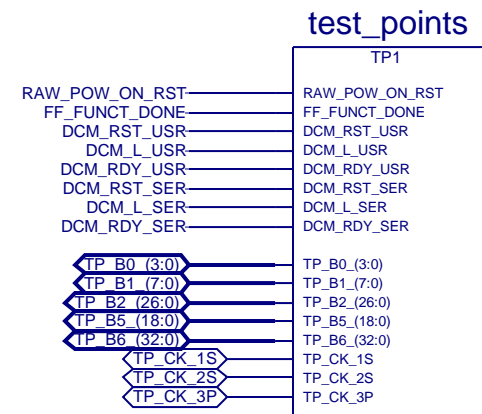
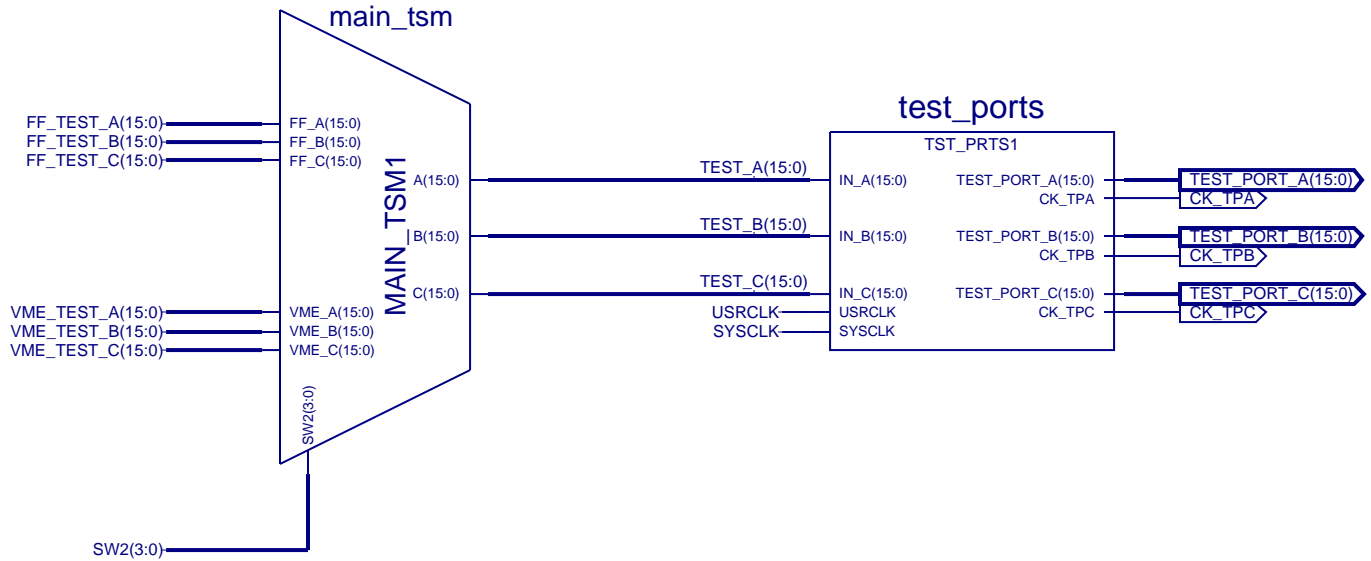
Project:D783C

Revision: 3.6F

Name: Gigabit Ethernet VME Controller

Date: Wed Nov 15 13:40:31 2006

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Title:VME Master and System Con	
Subtitle:Test Ports and Test Points	
Project:D783C	Revision: 3.6F
Name: Gigabit Ethernet VME Controller	
Date: Wed Nov 15 13:40:31 2006	Sheet 9 of 9