Fiber Installation Notes (Revision 2.2)

Scope:

DAQ path fiber optic cables.
From: Mini-rack fiber cassettes.
To: FED rack fiber cassettes in USC55 (S1G06, 07, and 08).

Notations and Conventions:

In USC55:

The fiber cassette crates in the FED racks are denoted as FCTC-1 through FCTC-12 where FCTC stands for Fiber Cassette Termination Crate.

In UXC55:

There is one fiber cassette crate per mini rack. So only the mini-rack label is used to specify the fiber cassette crate (ex: X5U41).

The cassette positions within a crate are numbered from 1 to 12 with position one starting on the left. Cassette positions are indicated in parentheses and appended to the crate name. So FCTC-7(3) specifies the cassette in position 3 of crate 7.

Connectors:

All connectors on the DAQ path fiber cassettes are to be LC receptacles.

Fiber Terminations:

The DAQ fibers are provided in one 24-fiber bundle per peripheral crate which means that there are two bundles per mini-rack (with the exception of YE3 (station 4) which only has one bundle per mini-rack).

Each 24-fiber bundle is terminated in a pair of 12-fiber cassettes on each end. On the mini-rack end, the cassettes in the pair are in adjacent positions in the crate. On the FED rack end, the cassettes in the pair are in the same position in two crates one directly above the other.

For YE1 and YE2, the mapping for terminating each fiber in a bundle is 1-to-1 with all fibers terminated. See the diagram and table in Fig. 1 and Fig. 2 for details.

For YE3, only 12 of the 24 fibers in the bundle are used and the mapping is also 1-to-1 but some cassette termination positions are left unconnected. See the diagram and table in Fig. 3 and Fig. 4 for details.

Mapping of Cassettes:

Table 1 specifies which mini-rack cassette is to be connected to each of the FED rack cassettes. For convenience the reverse mapping is listed in Table 3 (which FED rack cassette is to be connected to each mini-rack).

Observations:

1. This configuration of fiber terminations requires a total of 240 12-fiber cassettes and zero 6-fiber cassettes, which differs from the original quote.
2. With the exception of YE3, we are requesting that all 24-fibers in a bundle be terminated at both ends, which again differs from the original quote which only terminated the minimum number necessary.

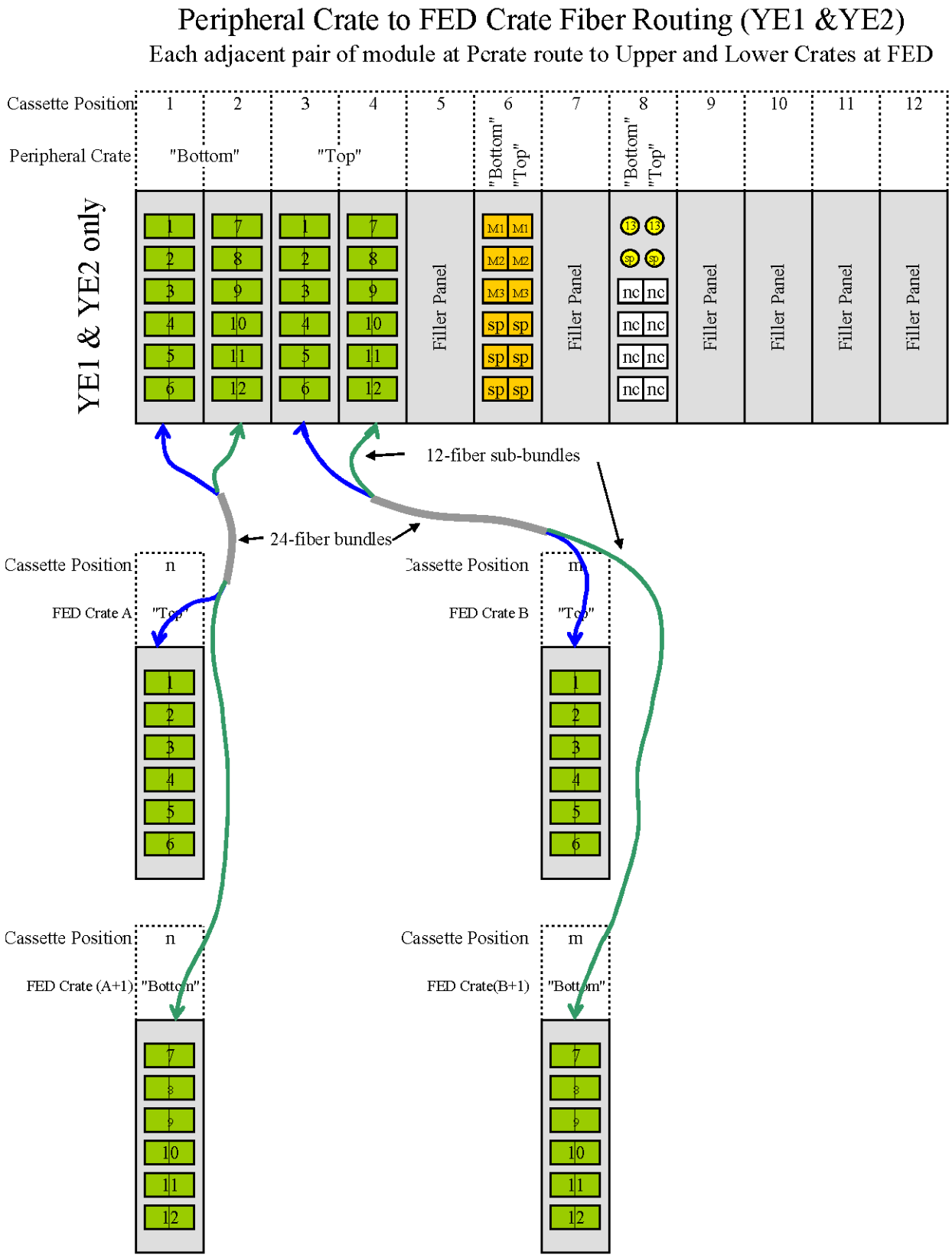
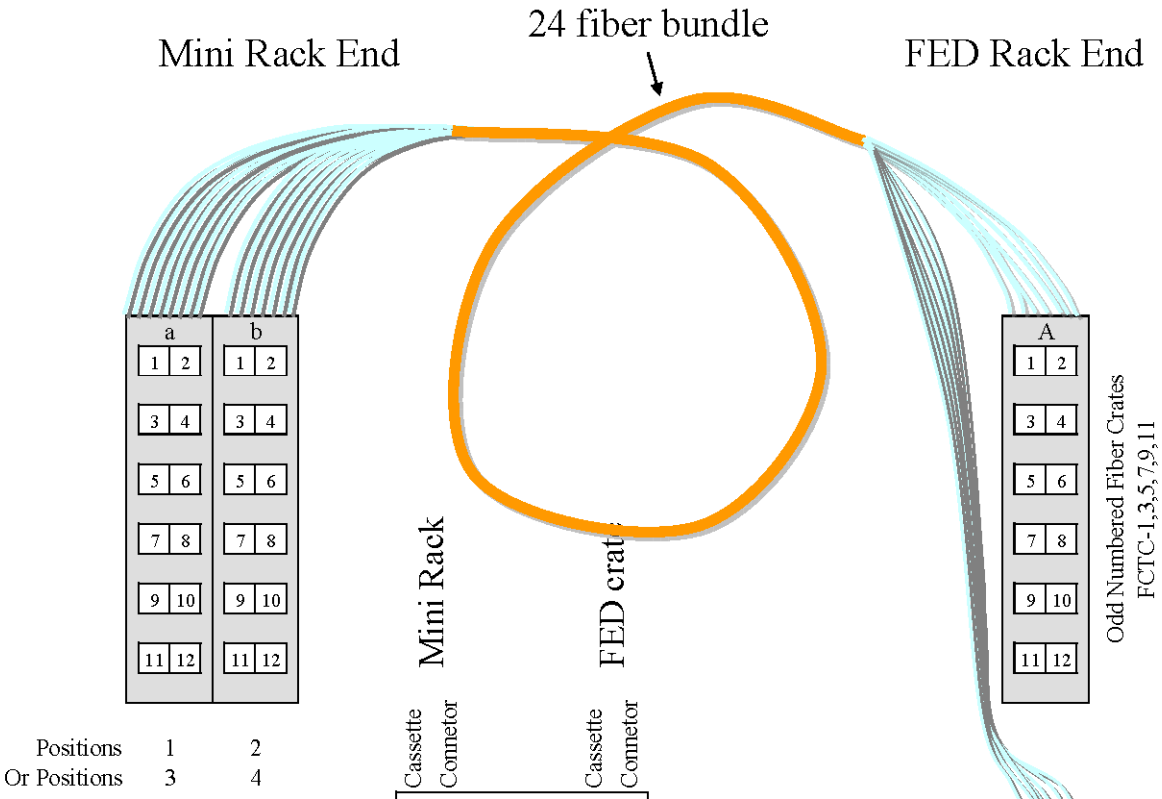

Peripheral Crate to FED Crate Fiber Routing (YE1 &YE2)
Each adjacent pair of module at Perate route to Upper and Lower Crates at FED
Cassette Position
Peripheral Crate
"Bottom"
"Top"
YE1 & YE2 only
Filler Panel
12-fiber sub-bundles
24-fiber bundles
FED Crate A
FED Crate B
FED Crate (A+1)
FED Crate(B+1)

Figure 1

EMU CSC DAQ Fiber Optics

Mapping of Fiber Connections per Bundle (YE1 & YE2)



Mini Rack / FED crate

Cassette	Connetor		Cassette	Connetor
a	1	→	A	1
a	2	→	A	2
a	3	→	A	3
a	4	→	A	4
a	5	→	A	5
a	6	→	A	6
a	7	→	A	7
a	8	→	A	8
a	9	→	A	9
a	10	→	A	10
a	11	→	A	11
a	12	→	A	12
b	1	→	B	1
b	2	→	B	2
b	3	→	B	3
b	4	→	B	4
b	5	→	B	5
b	6	→	B	6
b	7	→	B	7
b	8	→	B	8
b	9	→	B	9
b	10	→	B	10
b	11	→	B	11
b	12	→	B	12

Fiber mapping from cassette to cassette is one-to-one for YE1 & YE2 only.

Figure 2

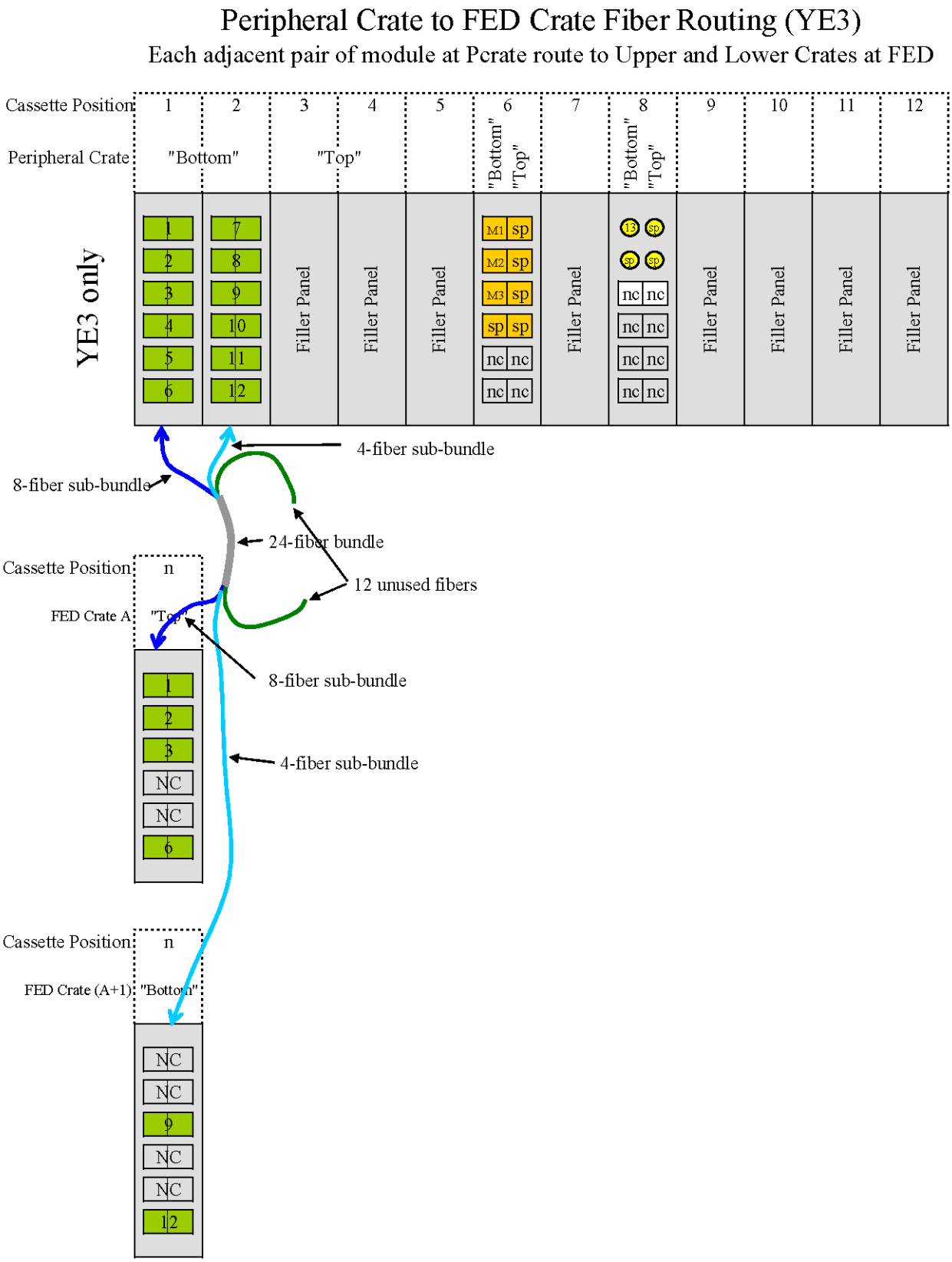
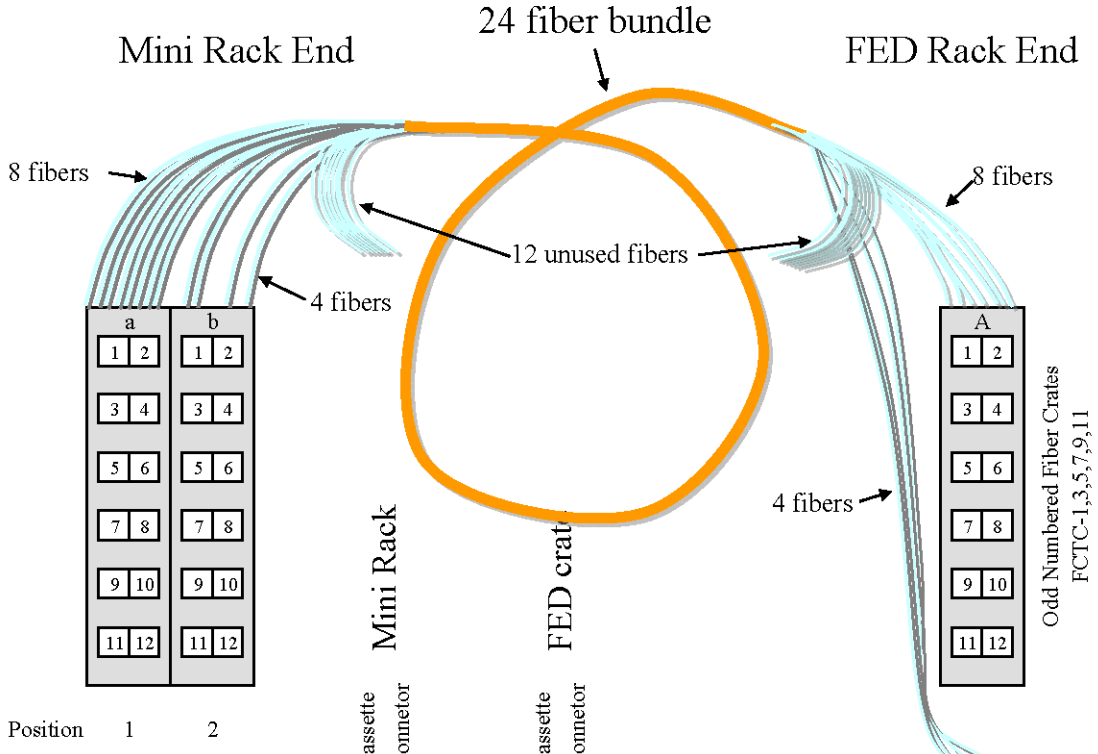

Peripheral Crate to FED Crate Fiber Routing (YE3)
Each adjacent pair of module at Pcrate route to Upper and Lower Crates at FED
Cassette Position
Peripheral Crate
"Bottom"
"Top"
"Bottom" "Top"
"Bottom" "Top"
YE3 only
Filler Panel
4-fiber sub-bundle
8-fiber sub-bundle
24-fiber bundle
12 unused fibers
Cassette Position n
FED Crate A "Top"
8-fiber sub-bundle
4-fiber sub-bundle
Cassette Position n
FED Crate (A+1) "Bottom"

Figure 3

EMU CSC DAQ Fiber Optics

Mapping of Fiber Connections per Bundle (YE3)



Cassette	Connetor		Cassette	Connetor
a	1	→	A	1
a	2	→	A	2
a	3	→	A	3
a	4	→	A	4
a	5	→	A	5
a	6	→	A	6
a	7	N/C	A	7
a	8	N/C	A	8
a	9	N/C	A	9
a	10	N/C	A	10
a	11	→	A	11
a	12	→	A	12
b	1	N/C	B	1
b	2	N/C	B	2
b	3	N/C	B	3
b	4	N/C	B	4
b	5	→	B	5
b	6	→	B	6
b	7	N/C	B	7
b	8	N/C	B	8
b	9	N/C	B	9
b	10	N/C	B	10
b	11	→	B	11
b	12	→	B	12

Fiber mapping from cassette to cassette for YE3 is one-to-one with some no-

Figure 4

Fiber Cassette Mapping for DAQ Path (by FED Crates)

FED			On Disk				
	Crate #	Cst. Pos.	Mini Rack	Cst. Pos.	Specification	Peripheral Crate	
FCTC4	4	1	X3A31	4	X3A31(4)	VME+1/	6
	4	2	X3A31	2	X3A31(2)	VME+1/	7
	4	3	X1R41	2	X1R41(2)	VME+2/	4
	4	4	X1U41	2	X1U41(2)	VME+3/	5
	4	5	X3J51	2	X3J51(2)	VME+4/	6
	4	6	N/A		N/A()		
	4	7	N/A		N/A()		
	4	8	X5E31	2	X5E31(2)	VME-1/	2
	4	9	X5E31	4	X5E31(4)	VME-1/	3
	4	10	X5L41	4	X5L41(4)	VME-2/	2
	4	11	X3S41	4	X3S41(4)	VME-3/	3
	4	12	X1L51	4	X1L51(4)	VME-4/	4
FCTC5	5	1	N/A		N/A()		
	5	2	X5L31	3	X5L31(3)	VME-1/	4
	5	3	X5L31	1	X5L31(1)	VME-1/	5
	5	4	X3S41	1	X3S41(1)	VME-2/	3
	5	5	X1L41	3	X1L41(3)	VME-3/	4
	5	6	X1E51	1	X1E51(1)	VME-4/	5
	5	7	N/A		N/A()		
	5	8	X3S31	3	X3S31(3)	VME-1/	6
	5	9	X3S31	1	X3S31(1)	VME-1/	7
	5	10	X1L41	1	X1L41(1)	VME-2/	4
	5	11	X1E41	1	X1E41(1)	VME-3/	5
	5	12	X3V51	1	X3V51(1)	VME-4/	6
FCTC6	6	1	N/A		N/A()		
	6	2	X5L31	4	X5L31(4)	VME-1/	4
	6	3	X5L31	2	X5L31(2)	VME-1/	5
	6	4	X3S41	2	X3S41(2)	VME-2/	3
	6	5	X1L41	4	X1L41(4)	VME-3/	4
	6	6	X1E51	2	X1E51(2)	VME-4/	5
	6	7	N/A		N/A()		
	6	8	X3S31	4	X3S31(4)	VME-1/	6
	6	9	X3S31	2	X3S31(2)	VME-1/	7
	6	10	X1L41	2	X1L41(2)	VME-2/	4
	6	11	X1E41	2	X1E41(2)	VME-3/	5
	6	12	X3V51	2	X3V51(2)	VME-4/	6

Fiber Cassette Mapping for DAQ Path (by FED Crates)

FED			On Disk				
	Crate #	Cst. Pos.	Mini Rack	Cst. Pos.	Specification	Peripheral Crate	
FCTC1	1	1	X5U31	1	X5U31(1)	VME+1/	2
	1	2	X5U31	3	X5U31(3)	VME+1/	3
	1	3	X5R41	3	X5R41(3)	VME+2/	2
	1	4	X3A41	1	X3A41(1)	VME+3/	3
	1	5	X1R51	3	X1R51(3)	VME+4/	4
	1	6	N/A		N/A()		
	1	7	X5R31	3	X5R31(3)	VME+1/	4
	1	8	X5R31	1	X5R31(1)	VME+1/	5
	1	9	X3A41	3	X3A41(3)	VME+2/	3
	1	10	X1R41	3	X1R41(3)	VME+3/	4
	1	11	X1U51	1	X1U51(1)	VME+4/	5
	1	12	N/A		N/A()		
FCTC2	2	1	X5U31	2	X5U31(2)	VME+1/	2
	2	2	X5U31	4	X5U31(4)	VME+1/	3
	2	3	X5R41	4	X5R41(4)	VME+2/	2
	2	4	X3A41	2	X3A41(2)	VME+3/	3
	2	5	X1R51	4	X1R51(4)	VME+4/	4
	2	6	N/A		N/A()		
	2	7	X5R31	4	X5R31(4)	VME+1/	4
	2	8	X5R31	2	X5R31(2)	VME+1/	5
	2	9	X3A41	4	X3A41(4)	VME+2/	3
	2	10	X1R41	4	X1R41(4)	VME+3/	4
	2	11	X1U51	2	X1U51(2)	VME+4/	5
	2	12	N/A		N/A()		
FCTC3	3	1	X3A31	3	X3A31(3)	VME+1/	6
	3	2	X3A31	1	X3A31(1)	VME+1/	7
	3	3	X1R41	1	X1R41(1)	VME+2/	4
	3	4	X1U41	1	X1U41(1)	VME+3/	5
	3	5	X3J51	1	X3J51(1)	VME+4/	6
	3	6	N/A		N/A()		
	3	7	N/A		N/A()		
	3	8	X5E31	1	X5E31(1)	VME-1/	2
	3	9	X5E31	3	X5E31(3)	VME-1/	3
	3	10	X5L41	3	X5L41(3)	VME-2/	2
	3	11	X3S41	3	X3S41(3)	VME-3/	3
	3	12	X1L51	3	X1L51(3)	VME-4/	4

Table 1

Fiber Cassette Mapping for DAQ Path (by FED Crates)

	FED Crate #	FED Cst. Pos.	On Disk Mini Rack	Cst. Pos.	Specification	Peripheral Crate	
FCTC10	10	1	X3J31	2	X3J31(2)	VME+1/	12
	10	2	X3J31	4	X3J31(4)	VME+1/	1
	10	3	X5U41	4	X5U41(4)	VME+2/	1
	10	4	X5R41	2	X5R41(2)	VME+3/	2
	10	5	X3A51	4	X3A51(4)	VME+4/	3
	10	6	N/A		N/A()		
	10	7	N/A		N/A()		
	10	8	X1L31	4	X1L31(4)	VME-1/	8
	10	9	X1L31	2	X1L31(2)	VME-1/	9
	10	10	X1E41	4	X1E41(4)	VME-2/	5
	10	11	X3V41	2	X3V41(2)	VME-3/	6
	10	12	X5E51	2	X5E51(2)	VME-4/	1
FCTC11	11	1	N/A		N/A()		
	11	2	X1E31	1	X1E31(1)	VME-1/	10
	11	3	X1E31	3	X1E31(3)	VME-1/	11
	11	4	X3V41	3	X3V41(3)	VME-2/	6
	11	5	X5E41	1	X5E41(1)	VME-3/	1
	11	6	X5L51	1	X5L51(1)	VME-4/	2
	11	7	N/A		N/A()		
	11	8	X3V31	1	X3V31(1)	VME-1/	12
	11	9	X3V31	3	X3V31(3)	VME-1/	1
	11	10	X5E41	3	X5E41(3)	VME-2/	1
	11	11	X5L41	1	X5L41(1)	VME-3/	2
	11	12	X3S51	3	X3S51(3)	VME-4/	3
FCTC12	12	1	N/A		N/A()		
	12	2	X1E31	2	X1E31(2)	VME-1/	10
	12	3	X1E31	4	X1E31(4)	VME-1/	11
	12	4	X3V41	4	X3V41(4)	VME-2/	6
	12	5	X5E41	2	X5E41(2)	VME-3/	1
	12	6	X5L51	2	X5L51(2)	VME-4/	2
	12	7	N/A		N/A()		
	12	8	X3V31	2	X3V31(2)	VME-1/	12
	12	9	X3V31	4	X3V31(4)	VME-1/	1
	12	10	X5E41	4	X5E41(4)	VME-2/	1
	12	11	X5L41	2	X5L41(2)	VME-3/	2
	12	12	X3S51	4	X3S51(4)	VME-4/	3

Fiber Cassette Mapping for DAQ Path (by FED Crates)

	FED Crate #	FED Cst. Pos.	On Disk Mini Rack	Cst. Pos.	Specification	Peripheral Crate	
FCTC7	7	1	X1R31	3	X1R31(3)	VME+1/	8
	7	2	X1R31	1	X1R31(1)	VME+1/	9
	7	3	X1U41	3	X1U41(3)	VME+2/	5
	7	4	X3J41	3	X3J41(3)	VME+3/	6
	7	5	X5U51	1	X5U51(1)	VME+4/	1
	7	6	N/A		N/A()		
	7	7	X1U31	1	X1U31(1)	VME+1/	10
	7	8	X1U31	3	X1U31(3)	VME+1/	11
	7	9	X3J41	1	X3J41(1)	VME+2/	6
	7	10	X5U41	1	X5U41(1)	VME+3/	1
	7	11	X5R51	1	X5R51(1)	VME+4/	2
	7	12	N/A		N/A()		
FCTC8	8	1	X1R31	4	X1R31(4)	VME+1/	8
	8	2	X1R31	2	X1R31(2)	VME+1/	9
	8	3	X1U41	4	X1U41(4)	VME+2/	5
	8	4	X3J41	4	X3J41(4)	VME+3/	6
	8	5	X5U51	2	X5U51(2)	VME+4/	1
	8	6	N/A		N/A()		
	8	7	X1U31	2	X1U31(2)	VME+1/	10
	8	8	X1U31	4	X1U31(4)	VME+1/	11
	8	9	X3J41	2	X3J41(2)	VME+2/	6
	8	10	X5U41	2	X5U41(2)	VME+3/	1
	8	11	X5R51	2	X5R51(2)	VME+4/	2
	8	12	N/A		N/A()		
FCTC9	9	1	X3J31	1	X3J31(1)	VME+1/	12
	9	2	X3J31	3	X3J31(3)	VME+1/	1
	9	3	X5U41	3	X5U41(3)	VME+2/	1
	9	4	X5R41	1	X5R41(1)	VME+3/	2
	9	5	X3A51	3	X3A51(3)	VME+4/	3
	9	6	N/A		N/A()		
	9	7	N/A		N/A()		
	9	8	X1L31	3	X1L31(3)	VME-1/	8
	9	9	X1L31	1	X1L31(1)	VME-1/	9
	9	10	X1E41	3	X1E41(3)	VME-2/	5
	9	11	X3V41	1	X3V41(1)	VME-3/	6
	9	12	X5E51	1	X5E51(1)	VME-4/	1

Table 1 (continued)

Fiber Cassette Mapping for DAQ Path
(by Peripheral Crates)

On Disk						FED			FiberBundle			
Trigger Sector		Mini Rack	Cst. Pos.	Peripheral Crate		Crate #	Cst. Pos.	Full Specification	bundle #	FED module		Pcrate module
Trig Sect 1	X5U31	X5U31	1	VME+1/	2	1	1	FCTC1(1)	1	top	FCTC1(1)	X5U31-1
		X5U31	2	VME+1/	2	2	1	FCTC2(1)	1	bottom	FCTC2(1)	X5U31-2
		X5U31	3	VME+1/	3	1	2	FCTC1(2)	2	top	FCTC1(2)	X5U31-3
		X5U31	4	VME+1/	3	2	2	FCTC2(2)	2	bottom	FCTC2(2)	X5U31-4
Trig Sect 2	X5R31	X5R31	1	VME+1/	5	1	8	FCTC1(8)	3	top	FCTC1(8)	X5R31-1
		X5R31	2	VME+1/	5	2	8	FCTC2(8)	3	bottom	FCTC2(8)	X5R31-2
		X5R31	3	VME+1/	4	1	7	FCTC1(7)	4	top	FCTC1(7)	X5R31-3
		X5R31	4	VME+1/	4	2	7	FCTC2(7)	4	bottom	FCTC2(7)	X5R31-4
Trig Sect 3	X3A31	X3A31	1	VME+1/	7	3	2	FCTC3(2)	5	top	FCTC3(2)	X3A31-1
		X3A31	2	VME+1/	7	4	2	FCTC4(2)	5	bottom	FCTC4(2)	X3A31-2
		X3A31	3	VME+1/	6	3	1	FCTC3(1)	6	top	FCTC3(1)	X3A31-3
		X3A31	4	VME+1/	6	4	1	FCTC4(1)	6	bottom	FCTC4(1)	X3A31-4
Trig Sect 4	X1R31	X1R31	1	VME+1/	9	7	2	FCTC7(2)	7	top	FCTC7(2)	X1R31-1
		X1R31	2	VME+1/	9	8	2	FCTC8(2)	7	bottom	FCTC8(2)	X1R31-2
		X1R31	3	VME+1/	8	7	1	FCTC7(1)	8	top	FCTC7(1)	X1R31-3
		X1R31	4	VME+1/	8	8	1	FCTC8(1)	8	bottom	FCTC8(1)	X1R31-4
Trig Sect 5	X1U31	X1U31	1	VME+1/	10	7	7	FCTC7(7)	9	top	FCTC7(7)	X1U31-1
		X1U31	2	VME+1/	10	8	7	FCTC8(7)	9	bottom	FCTC8(7)	X1U31-2
		X1U31	3	VME+1/	11	7	8	FCTC7(8)	10	top	FCTC7(8)	X1U31-3
		X1U31	4	VME+1/	11	8	8	FCTC8(8)	10	bottom	FCTC8(8)	X1U31-4
Trig Sect 6	X3J31	X3J31	1	VME+1/	12	9	1	FCTC9(1)	11	top	FCTC9(1)	X3J31-1
		X3J31	2	VME+1/	12	10	1	FCTC10(1)	11	bottom	FCTC10(1)	X3J31-2
		X3J31	3	VME+1/	1	9	2	FCTC9(2)	12	top	FCTC9(2)	X3J31-3
		X3J31	4	VME+1/	1	10	2	FCTC10(2)	12	bottom	FCTC10(2)	X3J31-4
Trig Sect 1	X5U41	X5U41	1	VME+3/	1	7	10	FCTC7(10)	13	top	FCTC7(10)	X5U41-1
		X5U41	2	VME+3/	1	8	10	FCTC8(10)	13	bottom	FCTC8(10)	X5U41-2
		X5U41	3	VME+2/	1	9	3	FCTC9(3)	14	top	FCTC9(3)	X5U41-3
		X5U41	4	VME+2/	1	10	3	FCTC10(3)	14	bottom	FCTC10(3)	X5U41-4
Trig Sect 2	X5R41	X5R41	1	VME+3/	2	9	4	FCTC9(4)	15	top	FCTC9(4)	X5R41-1
		X5R41	2	VME+3/	2	10	4	FCTC10(4)	15	bottom	FCTC10(4)	X5R41-2
		X5R41	3	VME+2/	2	1	3	FCTC1(3)	16	top	FCTC1(3)	X5R41-3
		X5R41	4	VME+2/	2	2	3	FCTC2(3)	16	bottom	FCTC2(3)	X5R41-4
Trig Sect 3	X3A41	X3A41	1	VME+3/	3	1	4	FCTC1(4)	17	top	FCTC1(4)	X3A41-1
		X3A41	2	VME+3/	3	2	4	FCTC2(4)	17	bottom	FCTC2(4)	X3A41-2
		X3A41	3	VME+2/	3	1	9	FCTC1(9)	18	top	FCTC1(9)	X3A41-3
		X3A41	4	VME+2/	3	2	9	FCTC2(9)	18	bottom	FCTC2(9)	X3A41-4

Table 2

Fiber Cassette Mapping for DAQ Path
(by Peripheral Crates)

On Disk						FED			FiberBundle			
Trigger Sector		Mini Rack	Cst. Pos.	Peripheral Crate		Crate #	Cst. Pos.	Full Specification	bundle #	FED module		Pcrate module
Trig Sect 4	X1R41	X1R41	1	VME+2/	4	3	3	FCTC3(3)	19	top	FCTC3(3)	X1R41-1
		X1R41	2	VME+2/	4	4	3	FCTC4(3)	19	bottom	FCTC4(3)	X1R41-2
		X1R41	3	VME+3/	4	1	10	FCTC1(10)	20	top	FCTC1(10)	X1R41-3
		X1R41	4	VME+3/	4	2	10	FCTC2(10)	20	bottom	FCTC2(10)	X1R41-4
Trig Sect 5	X1U41	X1U41	1	VME+3/	5	3	4	FCTC3(4)	21	top	FCTC3(4)	X1U41-1
		X1U41	2	VME+3/	5	4	4	FCTC4(4)	21	bottom	FCTC4(4)	X1U41-2
		X1U41	3	VME+2/	5	7	3	FCTC7(3)	22	top	FCTC7(3)	X1U41-3
		X1U41	4	VME+2/	5	8	3	FCTC8(3)	22	bottom	FCTC8(3)	X1U41-4
Trig Sect 6	X3J41	X3J41	1	VME+2/	6	7	9	FCTC7(9)	23	top	FCTC7(9)	X3J41-1
		X3J41	2	VME+2/	6	8	9	FCTC8(9)	23	bottom	FCTC8(9)	X3J41-2
		X3J41	3	VME+3/	6	7	4	FCTC7(4)	24	top	FCTC7(4)	X3J41-3
		X3J41	4	VME+3/	6	8	4	FCTC8(4)	24	bottom	FCTC8(4)	X3J41-4
Trig Sect 1	X5U51	X5U51	1	VME+4/	1	7	5	FCTC7(5)	25	top	FCTC7(5)	X5U51-1
		X5U51	2	VME+4/	1	8	5	FCTC8(5)	25	bottom	FCTC8(5)	X5U51-2
		X5U51	3	N/A							0	X5U51-3
		X5U51	4	N/A							0	X5U51-4
Trig Sect 2	X5R51	X5R51	1	VME+4/	2	7	11	FCTC7(11)	26	top	FCTC7(11)	X5R51-1
		X5R51	2	VME+4/	2	8	11	FCTC8(11)	26	bottom	FCTC8(11)	X5R51-2
		X5R51	3	N/A							0	X5R51-3
		X5R51	4	N/A							0	X5R51-4
Trig Sect 3	X3A51	X3A51	1	N/A							0	X3A51-1
		X3A51	2	N/A							0	X3A51-2
		X3A51	3	VME+4/	3	9	5	FCTC9(5)	27	top	FCTC9(5)	X3A51-3
		X3A51	4	VME+4/	3	10	5	FCTC10(5)	27	bottom	FCTC10(5)	X3A51-4
Trig Sect 4	X1R51	X1R51	1	N/A							0	X1R51-1
		X1R51	2	N/A							0	X1R51-2
		X1R51	3	VME+4/	4	1	5	FCTC1(5)	28	top	FCTC1(5)	X1R51-3
		X1R51	4	VME+4/	4	2	5	FCTC2(5)	28	bottom	FCTC2(5)	X1R51-4
Trig Sect 5	X1U51	X1U51	1	VME+4/	5	1	11	FCTC1(11)	29	top	FCTC1(11)	X1U51-1
		X1U51	2	VME+4/	5	2	11	FCTC2(11)	29	bottom	FCTC2(11)	X1U51-2
		X1U51	3	N/A							0	X1U51-3
		X1U51	4	N/A							0	X1U51-4
Trig Sect 6	X3J51	X3J51	1	VME+4/	6	3	5	FCTC3(5)	30	top	FCTC3(5)	X3J51-1
		X3J51	2	VME+4/	6	4	5	FCTC4(5)	30	bottom	FCTC4(5)	X3J51-2
		X3J51	3	N/A							0	X3J51-3
		X3J51	4	N/A							0	X3J51-4

Table 2 (continued)

Fiber Cassette Mapping for DAQ Path
(by Peripheral Crates)

On Disk						FED			FiberBundle			
Trigger Sector		Mini Rack	Cst. Pos.	Peripheral Crate		Crate #	Cst. Pos.	Full Specification	bundle #	FED module		Pcrate module
Trig Sect 1	X5E31	X5E31	1	VME-1/	2	3	8	FCTC3(8)	31	top	FCTC3(8)	X5E31-1
		X5E31	2	VME-1/	2	4	8	FCTC4(8)	31	bottom	FCTC4(8)	X5E31-2
		X5E31	3	VME-1/	3	3	9	FCTC3(9)	32	top	FCTC3(9)	X5E31-3
		X5E31	4	VME-1/	3	4	9	FCTC4(9)	32	bottom	FCTC4(9)	X5E31-4
Trig Sect 2	X5L31	X5L31	1	VME-1/	5	5	3	FCTC5(3)	33	top	FCTC5(3)	X5L31-1
		X5L31	2	VME-1/	5	6	3	FCTC6(3)	33	bottom	FCTC6(3)	X5L31-2
		X5L31	3	VME-1/	4	5	2	FCTC5(2)	34	top	FCTC5(2)	X5L31-3
		X5L31	4	VME-1/	4	6	2	FCTC6(2)	34	bottom	FCTC6(2)	X5L31-4
Trig Sect 3	X3S31	X3S31	1	VME-1/	7	5	9	FCTC5(9)	35	top	FCTC5(9)	X3S31-1
		X3S31	2	VME-1/	7	6	9	FCTC6(9)	35	bottom	FCTC6(9)	X3S31-2
		X3S31	3	VME-1/	6	5	8	FCTC5(8)	36	top	FCTC5(8)	X3S31-3
		X3S31	4	VME-1/	6	6	8	FCTC6(8)	36	bottom	FCTC6(8)	X3S31-4
Trig Sect 4	X1L31	X1L31	1	VME-1/	9	9	9	FCTC9(9)	37	top	FCTC9(9)	X1L31-1
		X1L31	2	VME-1/	9	10	9	FCTC10(9)	37	bottom	FCTC10(9)	X1L31-2
		X1L31	3	VME-1/	8	9	8	FCTC9(8)	38	top	FCTC9(8)	X1L31-3
		X1L31	4	VME-1/	8	10	8	FCTC10(8)	38	bottom	FCTC10(8)	X1L31-4
Trig Sect 5	X1E31	X1E31	1	VME-1/	10	11	2	FCTC11(2)	39	top	FCTC11(2)	X1E31-1
		X1E31	2	VME-1/	10	12	2	FCTC12(2)	39	bottom	FCTC12(2)	X1E31-2
		X1E31	3	VME-1/	11	11	3	FCTC11(3)	40	top	FCTC11(3)	X1E31-3
		X1E31	4	VME-1/	11	12	3	FCTC12(3)	40	bottom	FCTC12(3)	X1E31-4
Trig Sect 6	X3V31	X3V31	1	VME-1/	12	11	8	FCTC11(8)	41	top	FCTC11(8)	X3V31-1
		X3V31	2	VME-1/	12	12	8	FCTC12(8)	41	bottom	FCTC12(8)	X3V31-2
		X3V31	3	VME-1/	1	11	9	FCTC11(9)	42	top	FCTC11(9)	X3V31-3
		X3V31	4	VME-1/	1	12	9	FCTC12(9)	42	bottom	FCTC12(9)	X3V31-4
Trig Sect 1	X5E41	X5E41	1	VME-3/	1	11	5	FCTC11(5)	43	top	FCTC11(5)	X5E41-1
		X5E41	2	VME-3/	1	12	5	FCTC12(5)	43	bottom	FCTC12(5)	X5E41-2
		X5E41	3	VME-2/	1	11	10	FCTC11(10)	44	top	FCTC11(10)	X5E41-3
		X5E41	4	VME-2/	1	12	10	FCTC12(10)	44	bottom	FCTC12(10)	X5E41-4
Trig Sect 2	X5L41	X5L41	1	VME-3/	2	11	11	FCTC11(11)	45	top	FCTC11(11)	X5L41-1
		X5L41	2	VME-3/	2	12	11	FCTC12(11)	45	bottom	FCTC12(11)	X5L41-2
		X5L41	3	VME-2/	2	3	10	FCTC3(10)	46	top	FCTC3(10)	X5L41-3
		X5L41	4	VME-2/	2	4	10	FCTC4(10)	46	bottom	FCTC4(10)	X5L41-4
Trig Sect 3	X3S41	X3S41	1	VME-2/	3	5	4	FCTC5(4)	47	top	FCTC5(4)	X3S41-1
		X3S41	2	VME-2/	3	6	4	FCTC6(4)	47	bottom	FCTC6(4)	X3S41-2
		X3S41	3	VME-3/	3	3	11	FCTC3(11)	48	top	FCTC3(11)	X3S41-3
		X3S41	4	VME-3/	3	4	11	FCTC4(11)	48	bottom	FCTC4(11)	X3S41-4

Table 2 (continued)

Fiber Cassette Mapping for DAQ Path
(by Peripheral Crates)

On Disk						FED			FiberBundle			
Trigger Sector		Mini Rack	Cst. Pos.	Peripheral Crate		Crate #	Cst. Pos.	Full Specification	bundle #	FED module		Pcrate module
Trig Sect 4	X1L41	X1L41	1	VME-2/	4	5	10	FCTC5(10)	49	top	FCTC5(10)	X1L41-1
		X1L41	2	VME-2/	4	6	10	FCTC6(10)	49	bottom	FCTC6(10)	X1L41-2
		X1L41	3	VME-3/	4	5	5	FCTC5(5)	50	top	FCTC5(5)	X1L41-3
		X1L41	4	VME-3/	4	6	5	FCTC6(5)	50	bottom	FCTC6(5)	X1L41-4
Trig Sect 5	X1E41	X1E41	1	VME-3/	5	5	11	FCTC5(11)	51	top	FCTC5(11)	X1E41-1
		X1E41	2	VME-3/	5	6	11	FCTC6(11)	51	bottom	FCTC6(11)	X1E41-2
		X1E41	3	VME-2/	5	9	10	FCTC9(10)	52	top	FCTC9(10)	X1E41-3
		X1E41	4	VME-2/	5	10	10	FCTC10(10)	52	bottom	FCTC10(10)	X1E41-4
Trig Sect 6	X3V41	X3V41	1	VME-3/	6	9	11	FCTC9(11)	53	top	FCTC9(11)	X3V41-1
		X3V41	2	VME-3/	6	10	11	FCTC10(11)	53	bottom	FCTC10(11)	X3V41-2
		X3V41	3	VME-2/	6	11	4	FCTC11(4)	54	top	FCTC11(4)	X3V41-3
		X3V41	4	VME-2/	6	12	4	FCTC12(4)	54	bottom	FCTC12(4)	X3V41-4
Trig Sect 1	X5E51	X5E51	1	VME-4/	1	9	12	FCTC9(12)	55	top	FCTC9(12)	X5E51-1
		X5E51	2	VME-4/	1	10	12	FCTC10(12)	55	bottom	FCTC10(12)	X5E51-2
		X5E51	3	N/A							0	X5E51-3
		X5E51	4	N/A							0	X5E51-4
Trig Sect 2	X5L51	X5L51	1	VME-4/	2	11	6	FCTC11(6)	56	top	FCTC11(6)	X5L51-1
		X5L51	2	VME-4/	2	12	6	FCTC12(6)	56	bottom	FCTC12(6)	X5L51-2
		X5L51	3	N/A							0	X5L51-3
		X5L51	4	N/A							0	X5L51-4
Trig Sect 3	X3S51	X3S51	1	N/A							0	X3S51-1
		X3S51	2	N/A							0	X3S51-2
		X3S51	3	VME-4/	3	11	12	FCTC11(12)	57	top	FCTC11(12)	X3S51-3
		X3S51	4	VME-4/	3	12	12	FCTC12(12)	57	bottom	FCTC12(12)	X3S51-4
Trig Sect 4	X1L51	X1L51	1	N/A							0	X1L51-1
		X1L51	2	N/A							0	X1L51-2
		X1L51	3	VME-4/	4	3	12	FCTC3(12)	58	top	FCTC3(12)	X1L51-3
		X1L51	4	VME-4/	4	4	12	FCTC4(12)	58	bottom	FCTC4(12)	X1L51-4
Trig Sect 5	X1E51	X1E51	1	VME-4/	5	5	6	FCTC5(6)	59	top	FCTC5(6)	X1E51-1
		X1E51	2	VME-4/	5	6	6	FCTC6(6)	59	bottom	FCTC6(6)	X1E51-2
		X1E51	3	N/A							0	X1E51-3
		X1E51	4	N/A							0	X1E51-4
Trig Sect 6	X3V51	X3V51	1	VME-4/	6	5	12	FCTC5(12)	60	top	FCTC5(12)	X3V51-1
		X3V51	2	VME-4/	6	6	12	FCTC6(12)	60	bottom	FCTC6(12)	X3V51-2
		X3V51	3	N/A							0	X3V51-3
		X3V51	4	N/A							0	X3V51-4

Table 2 (continued)