

STATE MACHINE			
name	Startup_Display_FSM		
clock	CLK	posedge	
reset_signal	RST	posedge	
reset_state	Reset		
default_state_is_x	1		
INPUTS			
CLK			
RST			
TIMER[15:0]			
DONE			
RUN			
OUTPUTS			
DISP	1	regdp	
LOAD_PAT	0	regdp	
NXT_ADR	0	regdp	
CLEAR	0	regdp	
RST_TIMER	1	regdp	
STATES			
DISP	1	output	
LOAD_PAT	0	output	
NXT_ADR	0	output	
CLEAR	0	output	
RST_TIMER	1	output	
TRANSITIONS			
equation	1	def_type	
priority	1000		

